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<p>(54) Title: IMPROVED LOW MASS WAFER SUPPORT SYSTEM</p> <div data-bbox="370 1184 1360 1608"> </div> <p>(57) Abstract</p> <p>Improvements in the design of a low mass wafer holder are disclosed. The improvements include the use of peripherally located, integral lips to space a wafer or other substrate above the base plate of the wafer holder. A uniform gap is thus provided between the wafer and the base plate, such as will temper rapid heat exchanges, allow gas to flow between the wafer and wafer holder during wafer pick-up, and keep the wafer holder thermally coupled with the wafer. At the same time, thermal disturbance from lip contact with the wafer is reduced. Gas flow during pick-up can be provided through radial channels in a wafer holder upper surface, or through backside gas passages. A thicker ring is provided at the wafer holder perimeter, and is provided in some embodiments as an independent piece to accommodate stresses accompanying thermal gradients. A self-centering mechanism is provided to keep the wafer holder centered relative to a spider which is subject to differential thermal expansion.</p>		

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IMPROVED LOW MASS WAFER SUPPORT SYSTEM

Field of the Invention

The present invention relates to support structures for substrates in semiconductor processing chambers, and more particularly to low mass support structures for supporting wafers within a single-wafer processing chamber.

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Background of the Invention

High-temperature ovens, called reactors, are used to create structures of very fine dimensions, such as integrated circuits on semiconductor wafers or other substrates. A circular substrate, typically a silicon wafer, is placed on a wafer support. Both the wafer and support are heated, typically by a plurality of radiant lamps placed around a quartz chamber. In a typical process, a reactant gas is passed over the heated wafer, causing the chemical vapor deposition (CVD) of a thin layer of the reactant material on the wafer. Through subsequent processes, these layers are made into integrated circuits, with a single layer producing from tens to thousands of integrated circuits, depending on the size of the wafer and the complexity of the circuits. Other processes include sputter depositions, photolithography, dry etching, plasma processes, and high temperature anneals. Many of these processes require high temperature steps and can be performed in similar quartz reaction chambers.

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If the deposited layer has the same crystallographic structure as the underlying silicon wafer, it is called an epitaxial layer. This is also sometimes called a monocrystalline layer because it has only one crystal structure.

Various process parameters must be carefully controlled to ensure the high quality of the resulting layers. One such critical parameter is the temperature of the wafer during the processing. During CVD, for example, the deposition gas reacts at particular temperatures and deposits on the wafer. If the temperature varies greatly across the surface of the wafer, uneven deposition of the reactant gas occurs.

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In certain batch processors (i.e., reactors which process more than one wafer at a time) wafers are placed on a relatively large-mass susceptor made of graphite or other heat-absorbing material to help the temperature of the wafers remain uniform. In this context, a "large-mass" susceptor is one which has a large thermal mass relative to the wafer. The thermal mass of a solid, or its lumped thermal capacitance, is given by the equation:

$$C_T = \rho V c$$

25 where:

ρ - the density of the solid,

V - the volume of the solid, and

c - the specific heat (heat capacity) of the solid.

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Thus, the thermal mass is directly related to its mass, which is equal to the density times volume and to its specific heat.

One example of a large-mass susceptor is shown in U.S. Patent No. 4,496,609 issued to McNeilly, which discloses a CVD process wherein the wafers are placed directly on a relatively large-mass slab-like susceptor and maintained in intimate contact to permit a transfer of heat therebetween. The graphite susceptor supposedly acts

as a heat "flywheel" which transfers heat to the wafer to maintain its temperature uniform. The goal is to reduce transient temperature variations around the wafer that would occur without the "flywheel" effect of the susceptor.

Although large-mass susceptors theoretically aid in maintaining temperature uniformity across the wafers when the system is in a steady state, the large thermal mass of the susceptor makes the susceptor-wafer combination slow in responding to temperature transients (e.g., while heating up or cooling down the system). Accordingly, processing wafers with large-mass susceptors involves long thermal cycles, limiting the number of wafers which can be processed in a given length of time (i.e., limiting process throughput). High throughput remains a prime concern in single-wafer semiconductor processing.

In recent years, single-wafer processing of larger diameter wafers has grown for a variety of reasons including greater precision process control as compared to batch-processing. Typical wafers are made of silicon with one common size having a diameter of 200 mm and a thickness of .725 mm. Recently, larger silicon wafers having a diameter of 300 mm and a thickness of .775 mm have been introduced, as they even more efficiently exploit the benefits of larger single-wafer processing. Additionally, even larger wafers are contemplated for the future.

Although single-wafer processing by itself provides advantages over batch processing, control of the process parameters remains critical and is perhaps more so because of the increased cost of the larger wafers. One example of a single-wafer processor is shown in U.S. Patent No. 4,821,674, which utilizes a circular rotatable susceptor having a diameter slightly larger than the wafer. This susceptor is preferably made of graphite and has a lower thermal mass than the aforementioned slab-type batch processing susceptor. Nevertheless, the thermal mass of a production version of the susceptor described in U.S. Patent No. 4,821,674 is larger than the thermal mass of the single wafer, such that thermal cycle time for the system is limited.

U.S. Patent No. 4,978,567 describes a wafer holding fixture of lower mass than conventional susceptors. The lower mass facilitates rapid heating and cooling of the wafer for Rapid Thermal Processing (RTP) systems. Throughput can also be increased in connection with other processes involving heating or cooling of a substrate to be processed.

Processing wafers with such a low-mass wafer holder, however, introduces new problems. For example, the low mass of the wafer holder, combined with a small gap between the wafer and holder, makes it difficult to lift a wafer off the holder without also lifting the wafer holder. A vacuum effect causes the wafer and holder to stick together. As gas starts to fill the small gap, the gap will increase and the gas will flow faster. Accordingly, the holder will drop shortly after pick-up. Obviously such an uncontrolled drop can cause damage to the wafer holder and surrounding equipment within the reaction chamber. Additionally, particulate matter created by such damage can contaminate processed wafers.

The very rapidity of thermal response for which the low mass wafer holder is designed can also cause damage to the wafer and to reactor parts. For example, when first introduced into a reaction chamber, the wafer may be cold (e.g., 200°C), while the wafer holder remains hot (e.g., 900°C) from processing a prior wafer. Bringing the cold wafer into contact with a hot wafer holder causes a rapid heat drain from the holder to the wafer. The low mass wafer holder rapidly drops in temperature, as compared to the rate at which a high mass susceptor would

drop, until the wafer and wafer holder are in thermal equilibrium. The wafer, in the interim, undergoes a rapid heat influx. The rapid temperature fluctuation causes thermal shock to both the wafer and the holder. Both the wafer and holder tend to bow under the strain of vertical and radial temperature gradients during the transition. The stress can often cause breakage of the wafer holder and, occasionally, even the wafer.

5 The lower mass wafer holder is also susceptible to thermal expansion during heating. Due to differences in equipment material, the wafer holder will tend to expand at a different rate, as compared to surrounding equipment. In particular, a structure for supporting and rotating the wafer holder during processing is often constructed of quartz, such that radiant heat from below will largely pass through this structure. A typical graphite or silicon carbide (SiC) wafer holder expands significantly more rapidly than the quartz structure.

10 Relative movement between the supporting quartz and the wafer holder due to differences in thermal expansion can cause decentering of the wafer holder and the wafer upon it. Decentering, in turn, can tilt the wafer holder or otherwise upset a carefully balanced relationship between reactor elements and the wafer, configured for achieving temperature uniformity. Furthermore, eccentricity will exacerbate the decentration, such that the wafer holder can come in contact with a slip ring or other adjacent structure, bumping or rubbing against these structures during rotation and potentially introducing particulate matter into the reactor. Decentering can thus cause non-
15 uniformity in the quality and thickness of deposited layers, for instance.

Consequently, there is a need for an improved low mass wafer support structure to increase throughput of semiconductor processing devices while ensuring temperature uniformity across the wafer surface. Desirably, such a support structure should avoid the above-noted problems associated with wafer pick-up, thermal shock, and thermal
20 expansion.

Summary of the Invention

In accordance with one aspect of the present invention, a low mass wafer holder is provided for supporting a substrate within a process reactor. The holder has an upper surface and a lower surface, with a plurality of lips integral with the upper surface. Because the lips are integral, the lips can be machined to a uniform height above
25 the upper surface, thereby supporting the substrate with a uniform gap between substrate and the upper surface. In one embodiment, the upper surface is discontinuous, including the top surface of a peripheral supporting ring, as well as the top surface of a central base plate.

In accordance with another aspect of the present invention, a wafer holder for is provided for supporting a substrate within a process reactor. The wafer holder includes a central portion with an upper surface and a
30 plurality of spacers projecting a uniform height above the upper surface. The spacers are distributed to peripherally support the substrate above the upper surface. The holder also includes a fringe portion, including a ring inner wall extending upwardly from and surrounding the upper surface. The ring inner wall and the upper surface of the central portion thus together define a substrate pocket for accommodating the substrate. The peripheral location of the spacers minimizes any risk of thermal disturbance from the discrete spacer contact with the substrate being
35 processed.

In accordance with another aspect of the present invention, a semiconductor reactor is provided for treating a substrate. The reactor includes a reaction chamber, a plurality of heat sources, and a self-centering single-wafer support structure. The support structure is self-centering in the sense that it is centered and level at a first temperature as well as at a second, different temperature. The support structure includes a wafer holder for directly supporting the substrate, characterized by a first coefficient of thermal expansion. At least one recess is formed in a bottom surface of the wafer holder. The support structure also includes a support spider for supporting the wafer holder, characterized by a second coefficient of thermal expansion different from the first coefficient. The spider includes at least three support posts cooperating with the recess of the wafer holder.

In one embodiment, the support posts cooperate with three radial grooves formed in the bottom of the wafer holder, distributed at 60° intervals. In accordance with this embodiment, the wafer holder remains centered on the spider at any given temperature, despite differential thermal expansion of the holder relative to the spider. In another embodiment, each support post includes a hot-centering surface and a cold-centering surface. At high temperatures, the hot-centering surfaces of at least three support posts define a restrictive circle outside the peripheral edge of a base plate of the wafer holder. At low temperatures, the cold-centering surfaces of at least three support posts define a restrictive circle inside of the inner wall of a peripheral ring of the wafer holder.

In accordance with another aspect of the present invention, a low mass wafer holder is provided for supporting a single substrate in a processing chamber. The wafer holder includes a disc-shaped base plate and an annular ring independent of the base plate. The holder also includes an annular hanging portion integral with either the ring or the base plate, characterized by an inner diameter, and an annular supporting portion integral with the other of the ring and the base plate, characterized by an outer diameter smaller than the inner diameter of the hanging portion. The supporting portion underlies and supports the hanging portion. The two-piece wafer holder design has been found to alleviate stresses associated with thermal transfer for low mass wafer holders.

In accordance with another aspect of the present invention, a semiconductor reactor includes a reaction chamber, a plurality of heat sources, and a wafer support structure for supporting a wafer. The wafer support structure includes a low mass wafer holder, which directly supports the wafer. A temperature sensor, connected to at least one of the heat sources, senses the temperature at a point vertically spaced from the wafer holder. In one embodiment, a thermocouple is spaced below the wafer holder within a spacing range wherein the temperature reading is relatively insensitive to spacing changes. The wafer temperature can thereby be indirectly controlled at an appropriate temperature without regard to spacing changes caused by thermal expansion and other typical factors.

In accordance with another aspect of the present invention, a low mass wafer holder is provided for supporting a single substrate in a processing chamber. The wafer holder includes an upper support surface with a plurality of open radial channels. Each of the channels has a width less than the thermal diffusion length in the substrate (e.g., less than about 5 mm for a silicon substrate). The total volume of the channels is sufficient to permit lifting the substrate independently from the wafer holder. As the wafer is lifted, gas is permitted to flow beneath the wafer, such that no vacuum effect takes place and the wafer is easily separated from the wafer holder.

In accordance with another aspect of the present invention, a wafer holder is provided for supporting a substrate. The wafer holder has a thermal mass less than about five times the thermal mass of the substrate. The holder includes a base plate extending generally parallel with and spaced below the substrate. An annular lip peripherally supports the substrate above the base plate, and includes an inner face which defines a gap between the substrate and the base plate. A gas passage communicates from an underside of the wafer holder to the gap between the substrate and the base plate.

Brief Description of the Drawings

Further features and advantages of the present invention will become apparent from the detailed description of preferred embodiments which follow, when considered together with the attached drawings, which are intended to illustrate and not to limit the invention, and wherein:

Figure 1 is a schematic sectional view of an exemplary reaction chamber with a wafer supported therein by a support structure, including a one-piece wafer holder and a spider, constructed in accordance with a first preferred embodiment of the present invention;

Figure 2A is a partial, schematic sectional view of the wafer and support structure of Figure 1;

Figure 2B is a partial, schematic elevational end view, taken along line 2B-2B of Figure 2A;

Figure 3 is a graph illustrating bowing of the wafer holder and wafer from cold wafer placement on a hot wafer holder;

Figure 4 is a top plan view of the wafer holder of Figure 1;

Figure 5 is a partial enlarged view of a lip on the wafer holder of Figure 5;

Figure 6 is a partial elevational cross-section, taken along lines 6-6 of Figure 4;

Figure 7 is a partial elevational cross-section, taken along lines 7-7 of Figure 6;

Figure 8 is a bottom plan view of the wafer holder of Figure 1;

Figure 9 is a partial elevational cross-section, taken along lines 9-9 of Figure 8;

Figure 10 is a partial elevational cross-section, taken along lines 10-10 of Figure 9;

Figure 11 is a top plan view of the spider of Figure 1;

Figure 12 is an elevational cross-section, taken along lines 12-12 of Figure 11;

Figure 13 is an enlarged elevational view of a round-tipped post of the spider of Figure 11;

Figure 14 is a partial enlarged view of a central portion of the wafer and support structure of Figure 1, showing a center thermocouple spaced below the wafer holder;

Figure 15 is a graph plotting temperature readings of a thermocouple against the position of the thermocouple below the wafer holder of Figure 1;

Figure 16 is a partial, schematic sectional view of a wafer and support structure in accordance with a second preferred embodiment of the present invention;

Figure 17 is a top plan view of a support ring of the support structure of Figure 16;

Figure 18 is a partial enlarged view of a lip on the support ring of Figure 17;

Figure 19 is a partial elevational cross-section, taken along lines 19-19 of Figure 17;

Figure 20 is a partial elevational cross-section, taken along lines 20-20 of Figure 19;

Figure 21 is a bottom plan view of the support ring of Figure 16;

Figure 22 is a partial elevational cross-section, taken along lines 22-22 of Figure 21;

Figure 23 is a partial elevational cross-section, taken along lines 23-23 of Figure 22;

5 Figure 24 is a bottom plan view of a base plate of the support structure of Figure 16;

Figure 25 is a partial section view, taken along lines 25-25 of Figure 24;

Figure 26 is a partial, schematic sectional view of a wafer and support structure in accordance with a third preferred embodiment of the present invention;

Figure 27 is a partial top plan view of the wafer and support structure of Figure 26;

10 Figure 28 is a top plan view of a wafer holder, constructed in accordance with a fourth preferred embodiment of the present invention;

Figure 29 is a partial elevational cross-section, taken along lines 29-29 of Figure 28;

Figure 30 is a partial elevational cross-section, taken along lines 30-30 of Figure 28;

Figure 31 is a partial elevational cross-section, taken along lines 31-31 of Figure 28;

15 Figure 32 is a bottom plan view of the wafer holder of Figure 28;

Figure 33 is a top plan view of a support ring of a wafer holder, constructed in accordance with a fifth preferred embodiment of the present invention;

Figure 34 is a partial enlarged view of a bump on the support ring of Figure 33;

20 Figure 35 is a partial schematic sectional view of a wafer and base plate supported upon the support ring of Figure 33;

Figure 36 is a partial schematic sectional view of a wafer holder constructed in accordance with a sixth preferred embodiment of the present invention;

Figure 37 is a partial schematic sectional view of a wafer holder constructed in accordance with a seventh preferred embodiment of the present invention;

25 Figure 38 is a partial schematic sectional view of a wafer holder constructed in accordance with an eighth preferred embodiment of the present invention; and

Figure 39 is a partial schematic sectional view of a wafer holder constructed in accordance with a ninth preferred embodiment of the present invention.

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Detailed Description of the Preferred Embodiment

Exemplary System

Figure 1 illustrates an exemplary chemical vapor deposition (CVD) reactor 10, including a quartz reaction chamber 12. Radiant heating elements 14 are supported outside the chamber 12, to provide heat energy to the chamber 12 without appreciable absorption by the quartz chamber 12 walls. While the preferred embodiments are described in the context of a "cold wall" CVD reactor, it will be understood that the wafer support systems described herein will have utility in the context of reactors of other types. In particular, one of skill in the art can find

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application for the wafer support systems described herein for other semiconductor processing equipment, wherein a wafer is to be supported while being uniformly heated or cooled. Moreover, the term "wafer" support systems described herein can support any of a number of substrates, such as glass, which are to be subjected to treatment in reaction chambers, such as CVD, physical vapor deposition ("PVD"), etching, anneal, dopant diffusion, photolithographic, etc. The wafer supports are of particular utility for supporting substrates during treatment processes at elevated temperatures.

A wafer 16 with a generally annular edge 17 is shown supported within the reaction chamber 12 upon a wafer support structure 18, constructed in accordance with a first preferred embodiment of present invention. The illustrated support structure 18 includes a wafer holder 20, upon which the wafer 16 rests, and a support spider 22. The spider 22 is mounted to a shaft 24, which extends downwardly through a tube 26 depending from the chamber 12 lower wall.

A central temperature sensor or thermocouple 28 desirably mounts upon the spider 22 in proximity to the wafer holder 20. Additional peripheral thermocouples 30 are also shown, housed within a slip ring 32 which surrounds the wafer holder 20 and wafer 16. The thermocouples 28, 30 are connected to a temperature controller (not shown), which sets the power of the various heating elements 14 in response to the readings of the thermocouples 28, 30.

In addition to housing the thermocouples 30, the slip ring 32 absorbs radiant heat during high temperature processing. This compensates for a tendency toward greater heat loss at wafer edges 17, a phenomenon which is known to occur due to a greater concentration of surface area for a given volume near such edges. By minimizing edge losses and the attending radial temperature non-uniformities across the wafer 16, the slip ring 32 can prevent wafer crystallographic slip. The slip ring 32 can be suspended by any suitable means. For example, the illustrated slip ring 32 rests upon elbows 34 which depend from a front chamber divider 36 and a rear chamber divider 38. The dividers 36, 38 desirably are formed of quartz.

The illustrated reaction chamber 12 includes an inlet port 40 for the injection of reactant and carrier gases. An outlet port 42 is on the opposite side of the chamber 12, with the wafer support structure 18 positioned between the inlet 40 and outlet 42.

Low Mass Wafer Holders

The wafer holders described herein are "low mass" wafer holders in the sense that they have a thermal mass, as defined in the "Background" section above, comparable to that of the substrate to be supported, such that the temperatures of the wafer and wafer holder can change fairly quickly during heating and cooling processes. Preferably, the thermal mass of the wafer holder is less than about five times, more preferably less than about three times, and particularly between about 0.5 and two times the thermal mass of the substrate. The illustrated wafer holder 20 has a thermal mass about 1.7 times the thermal mass of the 200 mm wafer 16 shown. It will be understood that such thermal mass ratios can be approximately maintained for wafer holders for supporting wafers of other sizes.

The wafer holder 20 may comprise any of a number of suitable materials of consistent density, such as have been utilized in the past for susceptors. As the low mass holder 20 will generally be thin, however, a material exhibiting high strength is desirable. In particular, silicon carbide (SiC) is a preferred material which exhibits high strength, the ability to withstand high temperature cycling and typical CVD process gases, and can be provided with consistent density for uniform thermal properties. The purity with which a thin SiC wafer holder can be provided is also advantageous in preventing contamination of the reaction chamber 12 through normal use.

Single Piece Wafer Holder

Figures 2A and 2B schematically show a peripheral portion of the wafer 16 and wafer support structure 18. The wafer holder 20 of the first preferred embodiment comprises a central base plate 50 integrally connected to a peripheral ring 52. The ring 52 is defined by an inner wall 54, extending vertically from above a top surface 56 of the base plate 50, and an outer wall 58, and an upper surface 60 extending between the inner and outer walls. A bottom surface of the ring 52 includes a plurality of recesses 62 (one shown), preferably three, each of which cooperates with a spider post 64 for coupling the wafer holder 20 with the spider 22.

The wafer holder 20 further includes at least one and preferably a plurality of spacers or lips 66 (one shown) which directly support the wafer 16 upon the wafer holder 20. While one or two lips of adequate width can provide adequate support, such as the one continuous lip shown the embodiments of Figures 33-37 below, the wafer holder 20 preferably includes 3 to 12 lips. More preferably 6 to 9 lips 66, and particularly six, are desirable for 200 mm wafers. For 300 mm wafer support, 9 or more lips 66 are preferred, and at least one additional support (not shown) should be more centrally located (*e.g.*, within about 50 mm of the wafer center) to counter the effect of gravity on the more massive wafer. The height of the lips 66 define a gap between the wafer 16 and the base plate 50.

As noted in the "Background" section above, a low mass wafer holder tends to stick to the wafer during wafer pick-up after a processing step. This can be a problem for any pick-up device which lifts the wafer. For example, U.S. Patent No. 4,846,102 describes a pick-up wand which shoots high velocity streams of gas at angles. When brought close to the top of a wafer surface, the gas streams create a low pressure zone above the wafer, causing the wafer to lift. The disclosure of U.S. Patent No. 4,846,102 is hereby incorporated by reference. If the entire wafer bottom surface contacts a low mass wafer holder, a vacuum effect causes the wafer holder initially to lift along with wafer, then later dropping in an uncontrolled fashion.

The gap created by the preferred spacers or lips 66 can alleviate this concern by permitting gas to flow between the wafer 16 and the holder 20, thereby reducing the vacuum effect which can cause inadvertent lifting of the low mass wafer holder 20. The copending U.S. patent application Serial Number 08/621,627, filed March 26, 1996, entitled LOW MASS SUSCEPTOR, discloses a low mass wafer holder including a gap to facilitate wafer pick-up. The disclosure of this application is hereby expressly incorporated by reference. In that application, the spacers comprise separate pins which fit into recesses on the top surface of the wafer holder, distributed at points below the wafer.

As also noted in the "Background" section, introducing a cold wafer to a pre-heated susceptor or wafer holder can cause thermal shock to the wafer holder and/or wafer. The gap can also address this concern by slowing the transfer of heat from a hot wafer holder to a cold wafer.

Figure 3 illustrates, however, that the presence of a gap does not altogether alleviate the problem of bowing. Figure 3 shows wafer and wafer holder bowing, as a function of the wafer temperature, for a wafer at 200°C placed upon a disk-shaped wafer holder, of the same diameter as the wafer, at 900°C with a 250 micron (250×10^{-6} m) gap.

Immediately after the cold wafer is dropped upon the hot wafer holder, the temperature differential is at its greatest, as is the rate of heat exchange. Vertical thermal gradients are created across the susceptor and the wafer, causing both to bow down in the center to different degrees. The size of the gap thus changes, as also indicated upon the graphs of Figure 3. Furthermore, the gap change is greatest at the center of the wafer and least at the wafer edges 17. Accordingly, differential bowing can also cause lateral thermal gradients. Lateral thermal gradients are also induced when the wafer holder is larger in diameter than the wafer.

Table I shows that the wafer and wafer holder bow as a function of various factors. The columns list, in order: the temperature at which the wafer holder is upon cold drop-off, known as the "load temperature" of the wafer holder; the thickness of the portion of the wafer holder below the wafer; the size of the initial gap for various experimental arrangements; a time constant indicating the speed of the heat exchange between the wafer and the wafer holder (particularly, the time required to reach 63% of the total temperature change which occurs after cold wafer drop-off); the stress experienced by the wafer holder during the heat exchange; the degree of bowing experienced by the wafer holder; and the difference between bowing of the wafer and the wafer holder, given as a percentage of the initial gap.

TABLE 1

Load temp.	Plate Thickness	Gap	Time Constant	Stress	Plate Bow	Bow Difference
(°C)	(μm)	(μm)	(s)	(10^7 N/m^2)	(mm)	(% of Gap)
700	725	500	1.38	0.27	0.09	5
700	725	250	0.69	0.55	0.17	20
500	725	250	0.77	0.23	0.07	2
900	400	250	0.49	0.56	0.32	53
900	200	250	0.32	0.28	0.32	53
900	725	250	0.63	1.00	0.32	53
900	725	500	1.26	0.51	0.16	13
900	725	100	0.25	2.60	0.80	331
900	725	50	0.13	5.10	1.60	1324

As will be understood from Table I, a higher load temperature leads to a greater difference in bowing. Similarly, the thickness of the wafer holder and the size of the gap also affect the degree of stress and bow experienced. For a constant gap size, increasing thickness of the wafer holder slows achievement of thermal equilibrium but increases the stress upon the wafer holder, due to larger vertical thermal gradients. Increasing the size of the gap between the wafer and the wafer holder slows the heat exchange, as indicated by the time constant. This tempering of the heat exchange reduces thermal gradients, thereby reducing the stress upon the wafer holder. The bow difference, as a percentage of the gap, also decreases with increasing initial gap.

It will be understood that bowing of the preferred wafer holder will be different from that shown in Table I, because of its non-planar shape and lateral temperature gradients. The general relationship between bow and gap, however, remains true for the preferred wafer holder 20.

While Table I indicates that a large gap is beneficial for reducing stress during cold wafer drop-off, too large a gap can be disadvantageous for heating and cooling the wafer and holder together. For this consideration, a small gap is advantageous for thermally coupling the wafer and wafer holder. If the gap is too large and the wafer and wafer holder become uncoupled, the wafer temperature will not necessarily be close to the susceptor temperature and it will be difficult to control uniformity. As with wafer drop-off, thermal transients induce stress which can lead to stress-induced defects in the wafer, commonly referred to as crystallographic "slip."

Spacing Lips

Figures 4 to 10 illustrate an exemplary wafer holder 20 in accordance with the first preferred embodiment, constructed to accommodate the above-noted and other considerations. It should be noted, however, that the wafer holder 20 of Figures 4 to 10 is merely an example of a wafer holder within the scope of the present invention.

5 Along with the base plate 50, the inner wall 54 of the ring 52 defines a wafer pocket for receiving the wafer 16 (Figures 1 and 2) to be processed. The diameter of the inner wall 54 is thus slightly larger than the wafer 16 diameter, or about 7.970 inch (202.44 mm) for a 200 mm wafer 16. A chamfer 70, angled at about 45° to each of the ring top surface 60 and the inner wall 54, reduces the risk of injury to technicians in handling the wafer 16.

10 In order to provide the advantages of a low mass wafer holder, the thickness of the base plate 50 is desirably close to that of the wafer 16 to be processed. Accordingly, the base plate 50 preferably has a thickness between about 0.005 and 0.070 inch, and more preferably between about 0.010 and 0.040 inch. Typical 200 mm wafers have a thickness of about .029 inch. The base plate 50 of a wafer holder 20 designed to hold a 200 mm wafer is thus between about 0.025 and 0.032 inch. The illustrated base plate 50 has a thickness of about 0.0285 inch. In contrast, a wafer holder 20 designed for receiving a 300 mm wafer desirably has a base plate 50 thickness
15 of about 0.0305 inch. It will be understood that the thickness will be otherwise to maintain the desired thermal mass ratio for wafers of other sizes.

The thickness of the peripheral ring 52, as measured by the length of the outer wall 58, can be approximately equal to or greater than the thickness of the base plate 50, as shown. Preferably, the peripheral portion of the wafer holder 20 is between about 1.2 and 3.0 times as thick as the base plate 50, and more
20 preferably the ring 52 is about 2 times as thick as the base plate. For the illustrated embodiment, the ring 52 has a thickness of about 0.077 inch, whereas the base plate 50 has a thickness of about 0.0285. The greater mass of the peripheral ring 52 balances the normal tendency for the temperature at edges of the wafer holder 20 to lead the central portion of the base plate 50 during temperature changes. The ring 52 thus complements the edge loss compensation function performed by the slip ring 32 (Figure 1), at least to some extent.

25 Lip Height

As noted with respect to schematic Figure 2, the wafer holder 20 also includes a plurality of lips 66 to space the wafer 16 above the base plate 50. The height of the lips 66 is chosen to optimally promote heat coupling between the wafer 16 and the wafer holder 20, while still maintaining an adequate distance to minimize thermal shock upon wafer drop-off. It has been determined that the height of the lips 66 should be such as to permit a bow
30 difference of no more than about 20% of the gap height upon introduction of a cold wafer to a hot wafer holder. In addition to promoting heat coupling and minimizing thermal shock, the gap aids in separately picking up the wafer without the holder 20 sticking to the wafer, when lifting with a pick-up device which lifts the wafer 16 from above or from its edges 17.

In accordance with these guidelines, and considering the material and dimensions of the preferred wafer
35 holder 20, the lips 66 preferably extend above the base plate 50 a height of between about 0.005 and 0.080 inch, and more preferably between about 0.010 and 0.030 inch for processing 200 mm wafers. For 300 mm wafers, a

lip 66 height of about 0.015 to 0.050 inch is preferred, more preferably about 0.025 to 0.035 inch. The lips 66 of the illustrated embodiment are about 0.020 inch (about 500 microns) high. When the wafer 16 (Figures 1 and 2) rests upon the lips 66, a gap of 0.020 inch is thus created between the wafer 16 and the base plate 50.

Integral Lips

5 Desirably, the lips 66 are integrally formed as one piece with the surfaces of the wafer holder 20 from which they extend. In the illustrated embodiment, the ring 52, base plate 50 and lips 66 can all be machined from a single piece of silicon carbide. Fewer pieces are thus subject to loss during ordinary handling of the wafer holder 20 by the end user, as compared to separate support structures inserted into holes or recesses in a wafer holder. Also, integral spacers can not be lifted out of the wafer holder, unlike separate support structures, when a wafer
10 is unloaded from the holder after processing.

Perhaps more significantly, the lips 66 can be machined to a precisely uniform height. The wafer 16 is thereby evenly supported at a constant gap with the base plate 50 across the entire wafer surface. As will be understood by one of skill in the art, the uniform gap facilitates uniform thermal properties during processing at steady temperatures, as well as during temperature transitions.

15 An adequate number of lips 66 are provided and distributed to support the wafer 16. Thus, at least three lips 66 are provided. In the illustrated embodiment, designed to hold a 200 mm wafer 16, six lips 66 are shown circumferentially distributed at 60° intervals. For such an arrangement, a wafer of the type having a flat section for alignment purposes will be supported by at least three lips 66, and more likely by five lips 66, even if the flat is aligned with one of the lips 66.

Peripheral Lips

20 As shown, the lips 66 are advantageously positioned to peripherally support the wafer 16. Accordingly, the lips 66 extend radially-inward from the ring inner wall 54 a distance adequate to ensure that each of the lips 66 extend below the wafer 16 even if the wafer is not perfectly centered within the wafer pocket.

The term "peripheral," as used to describe the lips 66, indicates that the lips do not extend more than
25 about 5 mm inward of the wafer edge 17 when supporting a wafer 16, preferably extending from beyond the wafer edge 17 to less than about 3 mm inside the wafer edge 17. The illustrated lips 66 each extend about 0.125 inch (3.21 mm) from the inner wall 54 to an inner lip face 72. Thus, a circle 74 (Figure 4) defined by the lip faces 72 of the illustrated lips 66 has diameter of about 196 mm (7.720 inches), or slightly smaller than the diameter of the 200 mm wafer 16 to be supported. With the wafer 16 properly centered upon the wafer holder 20, each lip extends
30 only about 2.0 mm beneath the wafer 16.

Providing the support peripherally, however, advantageously tends to bring the support locations within the peripheral wafer exclusion zones (in which no devices are fabricated) of most manufacturers. As the exclusion zone of a 200 mm wafer is typically about 3 mm (0.12 inch), the lips 66 preferably extend less than this distance beneath the wafer 16 for typical wafer centering on the holder. Thus, any defects to the wafer 16 introduced by contact
35 with the lips 66 are in a less critical area of the wafer 16. Secondly, confining support locations to the periphery

presents a confined peripheral area in which to scan the wafer for crystallographic slip or other damage which can be caused by temperature non-uniformities.

Furthermore, as discussed above, vertical thermal gradients during ordinary processing, and consequent radial thermal gradients, tend to cause bowing of the wafer 16. A centrally supported wafer would curl upward at its edges. Such upward curling could scratch or otherwise damage the pick-up device or the wafer. The peripherally supported wafer 16, on the other hand, accommodates any wafer bowing in a central gap above the base plate 50.

Desirably, the lips 66 have a smooth upper surface finish such as to prevent backside damage to the wafer 16. The upper surface of the lips 66 preferably has a roughness of less than about 16 Ra, and more preferably less than about 8 Ra.

10 Width of Lips

Though peripherally located, preferably below the exclusion zone of the wafer 16 (Figure 2), the lips 66 should also be of small enough dimension to have negligible thermal effects upon the wafer 16 during processing. At least one dimension of the lip surface contacting the wafer 16 is thus preferably less than the thermal diffusion length in the material of the substrate to be processed. The thermal diffusion length is a measure of how far heat
15 diffuses into a material in a given period of time. More particularly, thermal diffusion length is given by the following formula:

$$L^2 = \frac{\lambda \tau}{C_p \rho}$$

where:

- λ = the heat conductivity of the material
- 20 τ = is a characteristic time constant,
- C_p = the specific heat (heat capacity) of the solid, and
- ρ = the density of the solid.

Taking $\tau = 0.55$, the 63% equilibration time for holder-wafer heat exchange, the diffusion length for silicon can be calculated as about 5 mm.

25 Preferably, therefore, at least one dimension of the contacting surface of each lip 66 is less than about 5 mm, more preferably less than about 3 mm, and most preferably less than about 1.5 mm.

More preferably, both dimensions of the lip surface contacting the wafer 16 in the supported position (see Figure 2) are less than the thermal diffusion length in silicon. The thermal effect of such small contacting surfaces is thus quickly averaged out, such that the no appreciable thermal gradients are created, even if the wafer holder
30 20 has a slightly different temperature than the wafer 16.

Therefore, each lip 66 has a width less than about 5 mm near the inner face 72 (see Figure 5). Preferably, the width of each lip 66 is between about 0.25 and 2.5 mm, more preferably about 0.5 and 1.5 mm, to provide stability for supporting the wafers while minimizing any disturbance to the wafer's thermal properties. The illustrated

embodiment provides a lip width of about 0.0285 inch (0.73 mm) at the lip inner face 72. As noted above, the length of each lip 66 (measured radially) is about 0.125 inch (3.2 mm), of which only about 2.0 mm extends below a properly centered wafer 16. Accordingly, both dimensions of the supporting surface of each lip 66 are less than the thermal diffusion length in silicon.

5

Curved Features

As seen from the views of Figures 5-7, the lips 66 include several curved surfaces in transition from the integral lip 66 to the remainder of the wafer holder 20. These curved surfaces provide increased strength to the lips 66, such as to resist breaking off during handling, wafer drop-off or wafer pick-up. For example, as best seen from the view of Figure 5, the illustrated lips 66 flare outwardly near the ring inner wall 54. The flare follows a radius of curvature between about 0.05 and 0.09 inch, preferably about 0.07 inch. This curvature particularly facilitates machining the preferred silicon carbide material. The width of the lip 66 at its juncture with the wall is thus about 0.11 inch (2.92 mm). As this base lip width remains less than the thermal diffusion length in silicon, the lips 66 will cause minimal thermal disturbance even for wafers which are not perfectly centered within the holder 20. Similarly, Figure 6 shows curved surfaces in transition between the ring inner wall 54 and the top surface of the lip 66, and between the lip inner face 72 and the top surface 56 of the base plate 50. These rounded corners have a radius of curvature around 0.010 inch, and extend all around the corners joining the illustrated lips 66 with the remainder of the wafer holder 20. In addition to providing strength to the small structures of the wafer holder 20, the illustrated curvature avoids sharp corners which can cause thermal and structural stresses.

While the preferred wafer holder 20 is designed for supporting a 200 mm wafer, one of ordinary skill in the art can readily adapt the teachings of the present disclosure to determine the appropriate dimensions for supporting for larger future generation wafers. For supporting a 300 mm wafer, for example, it is desirable to provide a larger gap than that for a 200 mm wafer. Preferably, the gap for a 300 mm wafer is between about 0.025 and 0.035 inch. It will additionally be understood that for larger wafers, a greater number of lips may be required to support a larger wafer without sagging in the center. Bumps or equivalent structures can also be provided in positions closer to the center of the wafer holder to support larger wafers and maintain constant separation between the wafer and the wafer holder.

Self-Centering Support System

Figures 8-10 illustrate the plurality of recesses 62 distributed about a bottom surface 80 of the wafer holder 20. As briefly noted with respect to Figure 2, each of the recesses 62 cooperates with a spider post 64 for coupling the wafer holder 20 with the spider 22 (Figures 11-13). Accordingly, the number of recesses 62 formed in the wafer holder 20 is desirably the same as the number of posts 64 of the spider 22 (Figure 11-13). In the illustrated embodiment, three recesses 62 are formed in the bottom surface 80 of the wafer holder 20, desirably even spaced at intervals of 120°, matching the spacing of the spider posts 64 (Figures 11-13) for centering the wafer holder 20 on the spider 22.

35

While each recess 62 is illustrated as aligned with one of the six lips 62 on the upper surface of the wafer holder 20 (see Figure 9), it will be understood that this alignment is not critical. Similarly, while each recess 62

is illustrated as extending peripherally to the outer wall 58, it will be understood that this position is not critical and is merely convenient from the standpoint of machining the recesses 62. Desirably, however, the recesses 62 are each located the same radial distance from the center of the preferred circular wafer holder 20.

Preferably, each recess 62 includes a level section 82, which is machined to a consistent depth for a pre-selected radial distance from the wafer holder 20 perimeter, thus running generally parallel to the major base plate surfaces (top and bottom). The pre-selected distance should be greater than the degree of relative movement between the wafer holder 20 and the spider 22 (Figures 11-13) caused by differential thermal expansion, as will be understood from the description of the recess centering function below. For the illustrated materials and dimensions, such relative movement will generally be less than about 0.025 inch. The radial length of the level section 82 is thus greater than about 0.025 inch, and is about 0.145 inch in radial length for the illustrated level section 82.

Radially inward of the level section 82 is a tapered section 84, which becomes more shallow in the direction of the center of the wafer holder 20. In the illustrated embodiment, the tapered section 84 conforms to a curvature of radius 1.40 to 1.50 inches. Advantageously, the tapering avoids sharp corners and attendant thermal stresses and facilitates manufacturing.

Figure 10 shows a profile of the illustrated recess 62. Preferably, the recess 62 includes a rounded trough portion 86, as shown, extending into a flat 88 in either circumferential direction. Thus, in the end view of Figure 10, the recess 62 resembles a "V" shape. Each flat 88 defines an angle α with the bottom surface 80 of the wafer holder 20. In the illustrated embodiment, the trough conforms to a curvature of radius 0.050 - 0.070 inch, and angle α is about 29°-31°. The deepest portion of the trough 86, within the level section 82, is about 0.0285 inch for the illustrated embodiment.

While the illustrated flats 88 are approximately planar, it will be understood in light of the interaction described below that the flats can have curvature. Preferably, the curvature does not exactly match that of the ball-end post 64 (described below) with which the recess 62 is to interact. More preferably, any curvature of the flat 88 is "flatter," or of higher radius, than that of the ball-end post 64.

25 Spider

Figures 11-13 illustrate the preferred spider 22. As noted, the recesses 62 in the bottom surface 80 of the wafer holder 20 are designed to interact with posts 64 of the spider 22. The spider 22 includes a central hub 90 and a plurality of arms 92 extending radially from the hub 90. Preferably, the spider 22 includes three evenly spaced arms 92 (i.e., at 120° intervals) of equal length. The arms 92 terminate at posts 64, one of which was schematically depicted in Figure 2. Desirably, the posts 64 define a cylinder of radius slightly larger than the wafer 16 to be supported, indicated in phantom in Figure 11. The spider 22 preferably comprises a material transparent to the radiation of the heat elements 14 (Figure 1). The illustrated spider 22 comprises quartz, which is both a radiation-transparent material and capable of withstanding repeated extreme heat cycling.

With reference to Figure 12, the hub 90 is hollow and includes an upper flange 94, a cylindrical portion 96 extending below the flange 94, and a conical portion 98 widening the hub 90 below the cylindrical portion 96. The conical portion 98 engages with the hollow shaft 24 (Figure 1), though it will be understood that the hub could

be integral with the shaft in other arrangements. The center thermocouple 28 and wiring therefor extends through the shaft 24, the hub 90, and upward through the opening defined by the flange 94. The center thermocouple 28 is illustrated and discussed in more detail with respect to Figures 14 and 15.

5 The arms 92 extend radially outward from the cylindrical portion 96 of the hub 90, generally horizontal and parallel with the wafer 16 in the properly mounted position (see Figure 1). Desirably, the thickness of each arm tapers radially outward. For example, the illustrated arm 92 has a thickness of about 0.40 inch near the hub, tapering to about 0.25 inch at the post. The posts 64 extend vertically upward from the radial terminus of each arm 92, a vertical length of 0.44 inch for the illustrated embodiment.

10 It will be understood that in other arrangements the spider arms can extend upwardly at an angle and still arrive at the same point as the horizontal arm and the vertical post. Even for such arrangements, however, it is advantageous to have at least a short vertical post at the terminus of the arm, as will be understood from a description of the ball-end of the preferred post 64 and its function.

As best seen from Figure 13, each post 64 terminates in a ball-end or rounded surface 100. The ball-end surface 100 is characterized by a size and curvature which is selected to fit within one of the recesses 62 in the wafer holder 20 (see Figures 8-10). For example, the illustrated ball-end surface 100 includes a central spherical section having a radius of curvature of about 0.09 - 0.11 inch and the width of the post 62 is about 0.157 inch. Radially outward of the central spherical section, the ball-end surface 100 also includes an annular conical section 101 which forms an angle β with the horizontal. The angle β is preferably greater than the angle α formed by the flats 88 (Figure 10) within the wafer holder recesses 62.

20 It will be understood that, for other arrangements, the ball-end surface can follow a curvature other than spherical (e.g., toroidal, elliptical, completely conical, pyramid-shaped, etc.). Desirably, however, the curvature is symmetrical in at least one dimension, for centering. While a vertical post section is advantageous in facilitating such symmetry, it will be understood that such symmetry can be provided in arrangements where the spider merely has arms angled upwardly from the hub.

25 Referring now to Figures 10 and 13 and schematic assembly Figures 2A and 2B, the preferred ball-end surface 100 (Figure 13) is larger than the trough portion 86 (about 0.060 inch) of the recesses 62, but still comes within the wider confines of the recess 62 defined by the flats 88 along each recess 62 (Figure 10). Accordingly, when assembled (Figures 2A and 2B), the spherical ball-end surface 100 of each spider post 64 contacts the flat surfaces 88 of one of the recesses 62 in the wafer holder 20.

30 During assembly, the radially inward tapered sections 84 (Figure 9) of the recesses 62 can aid the end user in centering the wafer holder 20 upon the spider 22. Once assembled, however, the ball-end 100 on each post 64 sits within the level section 82 of one of the recesses 62. There are three recesses 62 positioned around the bottom of the wafer holder 20 (preferably peripherally and at 120° to one another — see Figures 8 and 11), corresponding to three spider posts 64 similarly positioned relative to the spider hub 90.

35 When the three ball-end posts 64 are each within one of the three V-shaped recesses 62, the relative position of the spider 22 and the wafer holder 20 is constrained to one degree of freedom. For example, if an

external force is exerted which has a tendency to pull one post 64 in the direction along its corresponding recess 62, the outer flats 88 of the other two recesses 62 will exert an equal and opposite force on the other two corresponding ball-end posts 64 through the rounded surface 100. The movement in the direction of the external force is resisted, and the system remains centered. Since the one stable position available is arranged to center and level the wafer holder 20 (by positioning the various elements with radial symmetry), the combination of the ball-end surfaces 100 and the V-shaped recesses 62 is referred to as a self-centering system. The wafer holder 20 is centered relative to the spider 22 and the surrounding reactor parts (e.g., the slip ring 32).

It will be understood that self-centering in a unique position of stability occurs where more than three ball-end posts and V-shaped recesses are employed in a similar manner. Furthermore, one of skill in the art can readily apply the teachings of the present disclosure to reverse the positions of the curved surface and the V-shaped recess. For such an arrangement, each of three or more posts would include a V-shaped recess for cooperating with one of three or more curved surfaces provided on the bottom of the wafer holder.

In operation, the illustrated arrangement maintains a level and self-centered wafer holder 20 even when thermal expansion or contraction changes the dimensions of the wafer holder 20, and even when the wafer holder 20 expands or contracts to a different degree than the spider 22. Differential expansion generally occurs for the preferred materials, that is, for the quartz spider 22 and the SiC wafer holder 20, though it will be understood that such expansion would occur for parts of a variety of other suitable materials.

For example, at high temperatures, the wafer holder 20 will expand and the recesses 62 will move further away from the center of the wafer holder 20 (see Figure 8). For the preferred wafer holder dimensions and material, the movement may be as high as 0.025 inch. In the meanwhile, the posts 64 of the preferred quartz spider 22 will move relatively little, relative to the hub 90 (see Figure 11). Due to radial symmetry, the recesses 62 in an assembled wafer support structure 18 (Figures 1-2B), will each move radially outward by the same distance on their respective posts 64. At each temperature, there will be only one unique position of stability: the position in which the wafer holder 20 is centered.

Referring again to Figures 2A and 2B, the cooperation between the posts 64 and the recesses 62 can additionally impart rotation from the shaft 24 to the wafer holder 20. While the spider 22 may be stationary, it typically mounts to a rotating shaft to impart rotary motion to the wafer holder for enhancing chemical deposition uniformity over the wafer. In the illustrated embodiment, the shaft 24 is mechanically connected to a motor below the reaction chamber 12 (Figure 1).

Center Thermocouple Spacing

With reference to Figure 14, the center temperature sensor or thermocouple 28 is shown extending through the spider hub 90, with its tip 110 spaced from the base plate 50. The thermocouple 28 comprises an outer protective sheath surrounding a ceramic support, with a pair of thermocouple wires extending therethrough and forming a junction just below the tip 110.

Desirably, the protective sheath comprises a thermally conductive material which does not devitrify under CVD processing conditions. Preferably, the protective sheath is composed of silicon carbide, though other satisfactory

materials include boron nitride, silicon nitride, silicon dioxide, aluminum nitride, aluminum oxide, and various combinations thereof. The illustrated thermocouple 28 further includes a quartz sleeve or cap shielding the protective sheath from the junction, in the event the sheath is not chemically compatible with the thermocouple wires. Alternatively, a boron nitride coating can be used on the wires.

5 Prior art reactors typically include a center thermocouple to indirectly measure the temperature of the wafer holder, since methods of directly measuring wafer temperature during processing have been found unsatisfactory for a variety of reasons. Prior art wafer holders, however, typically were thick enough to include a recess in which to insert the thermocouple, thereby accurately obtaining a gauge of the wafer holder temperature. The wafer holder, in turn, was the best indicator of the actual wafer temperature. Knowing a relationship between wafer temperature
10 and wafer holder temperature through experimentation, the temperature controller can be calibrated to indirectly control wafer temperature.

The preferred base plate 50, on the other hand, is too thin to provide such a recess. A through hole is undesirable for the illustrated embodiment, as it would tend to induce backside deposition during CVD. Arranging the thermocouple tip 110 to contact the base plate 50 in order to obtain a reading as close as possible to the actual
15 temperature of the wafer holder 20 is also undesirable. Rotation of the wafer holder 20, vibrations, and/or thermal expansion could bring the thermocouple out of contact with the wafer holder 20 in the midst of processing, radically changing the relation thermocouple reading and the wafer temperature. Furthermore, the thermocouple 28 is typically fixed at a point on the order of 30 cm below the tip 110, beneath the reaction chamber 12. The length of the thermocouple 28, including the thermally conductive SiC protective sheath, expands upwardly from the fixed point
20 during thermal processing. This expansion can also cause the thermocouple tip 110 to come into and out of contact with the wafer holder 20, changing the thermocouple calibration, and potentially moving the low mass wafer holder 20 during processing.

Accordingly, the thermocouple 28 is mounted within the spider hub 90 to space the tip 110 a pre-determined distance from the wafer holder 20, as shown. At a minimum, the tip 110 is spaced from the wafer
25 holder 20 such that the two never touch during processing, even during high temperature steps where the upward thermal expansion of the thermocouple is at a maximum. Preferably, the maximum spacing is such that the wafer temperature deviates from the reading it would have if the thermocouple were touching the wafer holder by less than about $\pm 5^\circ\text{C}$, more preferably by less than about $\pm 2^\circ\text{C}$, and most preferably by less than about $\pm 1^\circ\text{C}$. Desirably, the pre-determined spacing between the tip 110 and the wafer holder 20 is chosen within a range wherein
30 the thermocouple/wafer temperature relationship remains fairly spacing insensitive, as will be understood from the following discussion of Figure 15.

Referring now to Figure 15, a graph is shown with wafer temperature on the abscissa and the spacing between the thermocouple 28 and the base plate 50 on the ordinate axis. As in actual reactor operation, a temperature controller connected to radiative heat sources (lamps) for the reactor chamber was used to maintain the
35 center thermocouple 28 temperature fixed at 1000°C for this experiment. An instrumented wafer supported by the wafer holder 20 was also heated by the heat lamps, and the temperature of the wafer directly measured by the

instrumentation in the wafer. As the center thermocouple 28 was moved, the wafer temperature changed, indicating that the relationship between the thermocouple temperature and wafer temperature changes with the distance between the wafer holder 20 and the thermocouple 28.

Between about 0.030 inch and 0.070 inch spacing, however, the curve remains fairly flat, as compared to adjacent sections of the curve. Within this range, the relationship between wafer and thermocouple temperature remains fairly spacing-insensitive. Accordingly, minor spacing fluctuations during processing from the above-noted factors (rotation of the wafer holder 20, vibrations, and/or thermal expansion) result in tolerable changes in the relationship between wafer temperature and thermocouple temperature. Therefore, the preferred wafer support structure 18 is arranged to space the tip 110 of the center thermocouple 28 between about 0.030 and 0.070 inch from base plate 50 of the preferred wafer holder 20. Preferably, the initial spacing is in the midst of this range, about 0.040 and 0.060 inch, such that fluctuations during operation remain within the 0.030 to 0.070 inch range. Most preferably, the initial spacing is set at about 0.050 inch.

One of skill in the art will recognize that the optimal thermocouple-wafer support spacing range may differ for different thermocouple types, and for different wafer support structures. For example, the optimal spacing may depend upon the thermocouple surface area exposed to the wafer holder, the surface area of the wafer holder, the lamp power, and/or other dimensions and materials of the thermocouple. The skilled artisan can adapt the teachings herein, however, to optimize the spacing for any particular system to minimize spacing fluctuation sensitivity, utilizing the above-described experimental techniques.

In particular, the initial spacing is preferably be such that the wafer temperature at steady state changes by less than about 10°C for a spacing fluctuation of 0.010 inch. In other words, the slope of a wafer temperature versus thermocouple spacing (like Figure 15) at the initial position of the thermocouple is within about $\pm 10^\circ\text{C}/0.010''$. More preferably, the slope is less within about $\pm 5^\circ\text{C}/0.010''$.

Two-Piece Wafer Holder With Supporting Ring

With reference now to Figures 16-25, a second preferred embodiment of the present invention is illustrated, wherein the wafer holder 20a comprises a ring 52a and a separate base plate 50a, rather than a single integral piece. The material of the illustrated wafer holder 20a can be the same as that of the previous embodiment, preferably comprising silicon carbide, and the lips 66a are still integral (forming one piece) with one of the ring 52a and base plate 50a. The wafer holder 20a is also preferably of low thermal mass, as described above. As the wafer holder 20a is otherwise substantially similar to the wafer holder 20 of the first preferred embodiment illustrated in Figures 1-15 and described above, like reference numerals will be used throughout the remaining description to describe like features. Similar but significantly altered features will be referred to with like reference numerals with the additional suffix "a." Additionally, new features without analogous features in the first embodiment will also carry the suffix "a."

With reference now to Figure 16, the wafer holder 20a is shown schematically, along with a post 64 which is as described above. As illustrated, the base plate 50a is suspended from the ring 52a. Suspension is provided by arranging a hanging portion of the base plate 50a to have a larger width or diameter (for the circular embodiment)

than a supporting portion of the ring 52a. The overlapping portions thus interact, with the hanging portion of the base plate 50a above the supporting portion of the ring 52a, to provide support for the base plate 50a.

As in the first preferred embodiment, the ring 52a includes a relatively thick band 200a below the upper surface 60 and between the inner wall 54 and outer wall 58. The ring 52a further includes a relatively less thick first annular shelf 202a, radially inward from and integral with the band 200a, upon which the plurality of lips 66 are positioned. Furthermore, the ring 52a includes a relatively even less thick second or subsidiary annular shelf 204a, radially inward from and integral with the first shelf 202a. For clarity, the second shelf 204a will be hereinafter referred to as a "step" 204a, while the first shelf 202a will be hereinafter referred to as the "shelf" 202a. The base plate 50a includes an annular overhang 205a.

It will be understood that, in the illustrated embodiment, the hanging portion of the base plate 50a comprises the overhang 205a, while the support portion of the ring 52a comprises the step 204a. Thus, in the illustrated embodiment, the support by these interacting portions is provided annularly, though the support can also be provided intermittently. The skilled artisan will recognize a number of manners in which to suspend the base plate from the ring. Preferably, however, the base plate 50a and ring 52a remain independent and are not permanently mounted to one another.

With reference now to Figures 17-23, the ring 52a of the second preferred embodiment is illustrated. The shelf 202a is partially defined by a horizontal shelf top surface 206a, extending between a vertical shelf inner wall 208a and the band inner wall 54. The step 204a is similarly partially defined by a horizontal step upper surface 210a, extending between a vertical step inner wall 212a and the shelf inner wall 208a. Preferably, the band 200a, shelf 202a and step 204a all share a common ring bottom surface 214a. The bottom surface 214a includes the recess 62, which can be substantially identical to that of the first preferred embodiment.

The dimensions of features not specifically described here can be the same as those noted above for similar features of the first preferred embodiment. Thus, for example, the illustrated band 200a has a thickness of about 0.077 inch, as with the thickness of the ring 52 of the first preferred embodiment (see, e.g., Figure 6), and the inner wall 54 diameter is about 7.970 inch for a 200 mm wafer, also as above.

The shelf 202a of the second preferred embodiment desirably has the same thickness as the base plate 50a (described below), and in the illustrated embodiment is about 0.0285 inch. The width of this shelf 202a, as measured along the shelf top surface 206a, is desirably the same as the lip 66, namely about 0.125 inch. This width is chosen for ease of machining, as the lip inner face 72 can be machined simultaneously flush with the shelf inner wall 208a. The height of shelf inner wall 208a, best seen from the view of Figure 22, defines the distance below the shelf 202a that the step 204a begins, and is about 0.0105 inch in the illustrated embodiment.

The step 204a should be thick enough to support the weight of the base plate 50a (Figures 24-25), and wide enough to ensure that the base plate 50a remains supported rather than falling through the central opening defined by the ring 52a. In the illustrated embodiment, the thickness of the step 204a, as measured along the step inner wall 212a, is about 0.018 inch. The width of the illustrated step 204a, as measured along the step upper

surface 210a, is about 0.030 inch. The inside diameter of the ring 52a, defined by the inner wall 212a, is about 7.660 inch.

As with the first preferred embodiment, corners between vertical and horizontal surfaces are each rounded with radii of about 0.010 inch, in order to improve strength.

5 With reference now to Figures 24-25, the base plate 50a of the second preferred embodiment is illustrated. The overhang 205a is partially defined by a vertical overhang wall 220a, extending downward from the base plate top surface 56a. A horizontal overhang lower surface 222a extends radially inward from the overhang wall 220a. A vertical lower base wall 224a extends downwardly from the overhang lower surface 222a.

10 The overall thickness of the base plate 50a is desirably the same as the shelf 202a of the ring 52a (Figures 17-23), or about 0.0285 inch for the illustrated embodiment, in order to provide a constant thickness of wafer holder material below the wafer 16.

The thickness of the overhang 205a is sufficient to support the weight of the entire base plate 50a by suspension upon the support portion (step 204a) of the ring 52a. Preferably, the thickness of the overhang 205a is the same as the height of the shelf inner wall 208a (see especially Figure 22), or about 0.0105 inch for the 15 illustrated embodiment. By providing an overhang thickness identical to the shelf inner wall height, a (discontinuous) wafer holder upper surface is advantageously provided at a constant distance from the wafer 16. The discontinuous wafer holder upper surface comprises the base plate top surface 56a together with the shelf top surface 206a (best seen in Figures 17 and 22) of the ring 52a.

20 The width of the overhang 205a is desirably about equal to the width of the step 204a upon which the overhang 205a rests in the assembled arrangement (see Figure 16). Thus, the illustrated overhang has width, as measured along the overhang lower surface 222a, of about 0.030 inch. The outside diameter of the plate 50a, defined by the overhang wall 222a, is about 7.700 inch, which is larger than the inside radius of the ring 52a by about 0.040 inch. The step 204a of the ring 52a and the lower surface 222a of the base plate 50a thus overlap but leave a leeway of about 0.010 inch on either side. The plate 50a can thus be adequately supported by the step 25 204a, while the leeway accommodates both manufacturer tolerances and permits the end user to fit the plate 50a into the ring 52a during assembly.

As with most other corners of the preferred embodiments, the corner between the overhang lower surface 222a and the base lower wall 224a is rounded with about a 0.010 radius. This curvature desirably matches the curvature of the corresponding corner on the ring between the step inner wall 212 and step upper surface 210a.

30 Advantageously, it has been found that the two-piece wafer holder 20a of the second preferred embodiment can withstand stresses associated with thermal gradients, such as the vertical gradients described above in connection with cold wafer drop-off. Accordingly, the two-piece wafer holder 20a survives a greater number of thermal cycles, such as those encountered during normal processing, than the one-piece wafer holder 20 of the first preferred embodiment.

35 The advantages of various other features not specifically described remain as described for the first preferred embodiment. Thus, for example, the lips 66 and recesses 62 are not described again here. Note, however,

that the recesses 62 of the second preferred embodiment are not aligned with lips 66, unlike the corresponding recesses 62 of the first preferred embodiment. A comparison of Figure 9, showing a lip 66 in the same section with a recess 62, with Figure 22, showing no lip in the section of the recess 62, makes this difference clear. The difference is not material, however, to the advantages described hereinabove.

5 **Two-Piece Wafer Holder With Supporting Base**

With reference now to Figure 26 and 27, a wafer support structure 18b is depicted schematically with a base-supported ring 52b, constructed in accordance with a third preferred embodiment of the present invention. The wafer support structure 18b also includes a self-centering mechanism, as described below. Desirably, the wafer support structure 18b comprises the same preferred materials as in the previous embodiments, namely SiC for the
10 wafer holder 20b and quartz for the spider 22b. The wafer holder 20b is also preferably of low thermal mass, as described above. Numerous features are substantially the same as corresponding features of the previous embodiment, such that like reference numerals will be used to describe like features. Similar but significantly altered features will be referred to with like reference numerals with the additional suffix "b." Additionally, new features without analogous features in the first embodiment will also carry the suffix "b."

15 In contrast to the second preferred embodiment, wherein the base plate 50a is suspended from the ring 52a (Figure 16), the ring 52b of the present embodiment is desirably suspended from the base plate 50b. Thus, a hanging portion of the ring 52b is arranged to have a smaller width or diameter (for the circular embodiment) than a supporting portion of the base plate 50b. The overlapping portions thus interact, with the hanging portion of the ring 52b above the supporting portion of the base plate 50b, to provide support for the ring 52b. The base plate
20 50b is independently supported by a spider 22b.

The base plate 50b of this embodiment can be a simple disc-shaped plate, as illustrated. Thus, the supporting portion of the base plate 50b is merely the peripheral portion of the plate 50b, just radially inward of a base plate peripheral edge 229b. In contrast to the second preferred embodiment, the plurality of lips 66b (preferably six) are formed on the base plate 50b. While illustrated schematically as separate pieces, lips 66b are
25 desirably formed integrally with the base plate 50b, for the same reasons as described with respect to the first preferred embodiment. Preferably, the lip height is within the ranges described above, as is the lip width.

The ring 52b includes a relatively thick band 200b, and at least one recess. In the illustrated embodiment, a plurality of peg recesses 230b are positioned radially inward of the band 200b. Each peg recess 230b is defined in part by a vertical recess back wall 232b and two vertical recess side walls 233b (best seen from the top down
30 view of Figure 27), each extending upwardly from the ring lower surface 214b. Preferably, the ring 52b includes three peg recesses 230b distributed circumferentially at about 120° to one another, for interacting with three spider posts 64b. The dimensions of each recess 230b are chosen for a self-centering interaction with the spider posts, described in more detail below.

Desirably, each peg recess 230b extends radially inwardly and opens into an annular groove, extending
35 around the inner lower perimeter of the ring 52b. The annular groove is defined by an overhanging ledge 234b and

a vertical groove inner wall 235b, as best seen from the view of Figure 27. The inner boundary of the ledge 234b is defined by the inner ring wall 54b, which has a diameter smaller than the outer diameter of the base plate 50b.

When assembled, the peripheral portion of the base plate 50b fits within the annular groove, such that the ledge 234b rests upon the peripheral portion of the base plate 50b, as shown. The groove inner wall 235b can be spaced slightly from peripheral edge 229b of the base plate 50b. Accordingly, in the illustrated embodiment, the overhanging portion of the ring 52b comprises the ledge 234b.

Hot and Cold Self-Centering

The base plate 50b is supported by a spider 22b, which can be similar in structure to the spider 22 of the first preferred embodiment. The spider posts 64b, however, have a different structure. Rather than the ball-end post, the spider posts 64b of the third preferred embodiment include an upward extension or protrusion with a hot centering surface and a cold centering surface. The hot centering surface desirably is proximate to or contacts an outer wall of the base plate (e.g., the base plate peripheral edge 229b) when the system is at high temperature, due to differential expansion of the base plate 50b (preferably SiC) relative to the spider 22b (preferably quartz). Similarly, the cold centering surface desirably is proximate to or contacts an inner wall of the ring 52b (e.g., the recess back wall 232b) when the system is at low temperature, due to differential contraction of the ring 52b relative to the spider 22b. "Proximate to," within the meaning of the present disclosure, means that the centering surface should be within about 0.020 inch of a vertical wafer holder surface, and more preferably within about 0.010 inch.

In the illustrated embodiment, the post end includes a platform 240b on the inner side of the post 64. The base plate 50b rests upon the platform 240b. For the cylindrical post, this platform 240b has the shape of a truncated circle (see Figure 27). At the radially outward edge of the platform 240b, a centering protrusion or peg 242b extends upwardly into the recess 230b of the ring 52b. The centering peg 242b includes an inner hot centering surface 244b and an outer cold centering surface 246b. In the illustrated embodiment, the cold centering surface 246b extends downwardly into an outwardly curved flared surface 248b, which in turn joins the vertical cylindrical outer surface of the main portion of the spider post 64b.

As noted, the hot centering surface 244b is proximate to or contacts the peripheral edge 229b of the base plate 50b during high temperature steps, for example at about 1200°C. It will be understood that, because at least three such hot centering surfaces 244b on at least three different spider posts 64b are distributed about the periphery of the base plate 50b, the base plate 50b is confined to negligible movement. Accordingly, the integral lips 66b which support the wafer 16 are also confined. The ring 52b rests upon the base plate 50b and has its movement limited by interaction of the lips 66b with the ring inner wall 54b, and/or by the interaction between the groove inner wall 235b and the peripheral edge 229b of the base plate 50b, depending on which two surfaces have the closer spacing.

Similarly, the cold centering surface 246b is proximate to or contacts the recess back wall 232b of the ring 52b during lower temperature steps, e.g., 20°C. It will be understood that, because at least three such cold centering surfaces 246b (on three different spider posts 64b) each contact one of at least three recess back walls

232b distributed about the ring 52b, the ring 52b is confined to negligible movement. The ring 52b, in turn, constrains movement of the base plate 50b and the wafer 16 thereon by interaction of the groove inner wall 235b with the peripheral edge 229b of the base plate 50b, or by interaction of the ring inner wall 54b with the lips 66b.

5 In the illustrated embodiment, the recess side walls 233b can interact with the spider posts 64b to prevent rotation of the ring 52b relative to the spider 22b, and can "clock" or ensure transfer of rotational drive from the spider 22b to the ring 52b, such that they rotate together. Friction between the ring 52b and the base plate 50b, and between the base plate 50b and the spider post platform 240b, couples the ring rotation to the base plate 50b, such that the entire wafer holder 20b (and the wafer held thereon) rotates together with the spider 22b.

10 It will be understood, however, that rotation of the spider can be transferred to the wafer holder without the illustrated recess structure. For example, a radially wider ledge can be formed by extending the annular groove radially outward to form a groove inner wall at the same radial distance at which the recess back wall 232b is illustrated in Figures 26-27. In such an alternative arrangement, the groove inner wall would interact with the cold centering surface 246b of the spider post 64b under lower temperature conditions, similarly to the centering function as described for the illustrated embodiment. In such an alternative arrangement, friction between the spider post platform and the base plate lower surface could adequately couple the spider and the base plate for purposes of rotation. The base plate, in turn, would also be frictionally coupled to the ring by the weight of the ring concentrated upon the peripheral portion of the base plate at the extended annular ledge.

Wafer Holder With Upper Gas Passages

20 With reference now to Figures 28-32, a wafer holder 20c, constructed in accordance with a fourth preferred embodiment of the present invention, facilitates wafer pick-up. Desirably, the wafer support structure comprises the same preferred materials as in the previous embodiments, namely SiC for the wafer holder 20c and quartz for the spider (not shown). The wafer holder 20c is also preferably of low thermal mass, as described above. Numerous features are similar or substantially the same as corresponding features of the previous embodiment, such that like reference numerals will be used to describe like features. Similar but significantly altered features will be referred to with like reference numerals with the additional suffix "c." Additionally, new features without analogous features in the previous embodiments will also carry the suffix "c."

25 As with the first preferred embodiment, the wafer holder 20c is a unitary piece comprising a central base plate 50c, with a relatively more thick peripheral annular ring 52c. The wafer (not shown) can fit within a basin defined by the thicker ring 52c. Structures are also provided to permit gas between the wafer and the wafer holder 20c. Like the lips of the previously described embodiments, the structures include dimensions selected to prevent undue thermal disturbance in the wafer. In the illustrated embodiment, gas fills gas passages formed in an upper surface of the wafer holder.

30 In particular, a plurality of open radial channels 250c are provided in the top surface of the wafer holder 250c to serve as gas passages. In the illustrated embodiment, each channel 250c comprises a base plate channel 252c and a ring channel 254c. This configuration advantageously allows machining each channel 250c at a constant

depth across the entire wafer holder 20c. Moreover, the ring channel 254c provides a large gas flow path to the base plate channel 252c for gas to enter as the wafer is picked up.

5 The base plate channels 252c, which are directly below the wafer just before wafer pick-up, have a width less than the thermal diffusion length in silicon (about 5 mm). Accordingly, the thermal effect upon the wafer, from the absence of material within the channels 252c, is negligible. Preferably, the base plate channels 252c have a depth of about 0.010 inch and a width of about 0.015 inch. It will be understood that the illustrated ring channels 254c, machined to the same level as the base plate channels 252c, are both deeper and wider (compare Figures 29 and 30). The thermal effect of the ring channels 254c, however, is attenuated relative to the base plate channels 252c.

10 Because the base plate channels 252c are so narrow, a plurality of channels 252c is provided to give a total volume of gas passages which is adequate for separately lifting the wafer. In the illustrated embodiment, 32 such channels 252c are provided.

15 The one-piece wafer holder 20c includes a common lower surface 80c. Three or more recesses 62c are provided in the lower surface 80c. The recesses 62c are provided for interaction with a similar number of spider posts (not shown), which can support and impart rotation to the wafer holder 20c, as described with respect to the previous embodiments. The spider posts can be configured with hot- and cold-centering surfaces spaced to anticipate differential thermal expansion and contraction, as will be understood by one of skill in the art in light of the above disclosure with respect to the third preferred embodiment.

20 Additionally, a central opening 256c is provided through the wafer holder 20c. Desirably, the central opening 256c is sized to receive a thermocouple. The thermocouple can thereby read the temperature in close proximity to the wafer during processing, and the heat lamps outside the reaction chamber can be controlled accordingly. Furthermore, as the thermocouple extends through a central hub on a spider (see Figure 14), the thermocouple can keep the wafer holder 20c centered during rotation and thermal cycling, regardless of thermal expansion.

25 Wafer Holders With Lower Gas Passages

Figures 33-37 illustrate wafer holders which provide one or more lower gas passages. The lower gas passages allow gas to flow into the gap between the base plate and the wafer while the wafer is being picked up, and out of the gap while the wafer is being dropped off. In each of the illustrated embodiments, the support ring is provided with a single annular lip with a cylindrical inner face, rather than a plurality of spaced lips. The annular lip still spaces the wafer above a base plate. It will be understood, however, that in other arrangements the backside gas flow can be combined with the front side gas flow between spaced lips, such that adequate gas flow is ensured between the wafer and wafer holder during pick-up and drop-off.

35 With reference to Figures 33-35, a two-piece wafer holder 20d is illustrated in accordance with a fifth preferred embodiment. As with previous embodiments, the wafer holder 20d desirably comprises the same preferred materials as in the previous embodiments, namely SiC for the wafer holder 20d and quartz for the spider (not shown). The wafer holder 20d is also preferably of low thermal mass, as described above. Numerous features are

similar or substantially the same as corresponding features of the previous embodiment, such that like reference numerals will be used to describe like features. Similar but significantly altered features will be referred to with like reference numerals with the additional suffix "d." Additionally, new features without analogous features in the previous embodiments will also carry the suffix "d." Similarly, reference numerals for altered or new features of
5 a sixth preferred embodiment will carry the suffix "e" in Figure 36.

Referring again to Figures 33-35, preferably a single annular lip 66d is provided, such as to effectively seal the front side of the wafer 16 from the back side. Such an arrangement effectively eliminates the diffusion of processing gas to the backside from the front side of the wafer 16. This seal would ordinarily create a vacuum effect during wafer pick-up. In the illustrated embodiment, however, an annular backside passage 260d (Fig. 35)
10 is provided between the ring 52d and the base plate 50d, and specifically between ring step 204d and the base plate overhang 205d. As a wafer is picked up after processing, gas is drawn up from below the wafer holder through the backside passage 260d, preventing the base plate 50d from sticking to the wafer 16 by a vacuum effect. For example, purge gas will generally be present below the wafer holder 20d, both during and after processing. It will be understood that the backside passage 260d similarly allows gas flow outward (downward) from the gap during
15 wafer drop-off.

The backside passage 260d is provided in the illustrated embodiment by a plurality of bumps 262d positioned about the upper surface 210d of the ring step 204d. Desirably, more than three bumps 262d are provided to support the base plate 50d levelly, while the number and size of the bumps are small enough to permit sufficient gas flow through the passage 260d for the wafer 16 to be picked up independently of the base plate 50d.
20 Preferably six or more bumps are provided, while twelve such bumps 262d are illustrated in the plan view of Figure 33. The thickness of the ring step 204d and the base plate overhang 205d can be adjusted, within the dictates of structural strength requirements, to maintain the gap between the base plate 50d and the wafer 16, as described above, as will be understood by one of skill in the art.

While the backside passage 260d of the illustrated embodiment allows gas to be drawn through to the gap
25 between the wafer 16 and the wafer holder 20d during wafer pick-up, gas will tend not to flow during processing, as the front side is effectively sealed by contact between the wafer 16 and the annular lip 66d. Combined with the fact that the preferred reactor 10 provides mostly purge gas below the wafer holder 20d, the lack of flow effectively eliminates the ability of deposition gases (or other reactant species) to reach the backside of the wafer.

Advantageously, the arrangement illustrated in Figure 35 provides the backside passage 260d in a stepped
30 path, such that radiant heat does not directly penetrate from lamps to the wafer 16 and hot spots are avoided.

Referring now to Figure 36, a wafer holder 20e is shown in accordance with a sixth preferred embodiment of the present invention. A lower gas passage is provided in the form of at least one conduit 270e leading from the underside of the wafer holder 20e to the gap between the base plate 50e and the wafer 16. Preferably, the conduit 270e leads from the underside of the ring 52e to the side wall of the annular lip 66e. Similarly to the
35 annular backside passage 260d of the previously described embodiment, the conduit 270e allows gas to fill the gap while the wafer is being lifted, thus reducing any vacuum effect which could otherwise cause the baseplate 50e to

be lifted with the wafer. Similarly, the conduit 270e can also allow gas to exit the gap between the wafer 16 and base plate 50e during wafer drop, thus preventing the wafer from floating off center.

Desirably, three conduits 270e are provided, aligned with posts 64e of a spider 22e. Each of the conduits 270e connects to a hollow spider post 64e, through which gas can flow, at least during wafer pick-up and desirably also during wafer drop-off. Preferably, a purge gas line is connected to the hollow spider 22e, accessing purge gas.

The purge gas line is most preferably connected to the same gas line flowing purge gas to a pick-up wand of the type described in U.S. Patent No. 4,846,102. Thus, in the preferred embodiment, the purge gas would flow through the spider post 64e and the conduit 270e only during when the pick-up wand is operating, such that no flow would be forced into the gap during wafer processing. Overpressure from the gas flow can also be avoided by providing obstructions in the hollow spider 22e, to restrict gas flow into the wafer-base plate gap.

Apart from the conduit 270e and the annular lip 66e, the wafer holder 20e can be as described with respect to the second preferred embodiment.

Desirably, the conduits 270e are angled inwardly as shown, such as to avoid lamp radiation directly falling on the wafer.

Figure 37 illustrates a wafer holder 20f constructed in accordance with a seventh preferred embodiment, incorporating features of both the fifth and sixth preferred embodiments. By providing both conduits 270e and the annular backside passage 260d, the wafer holder 20f can provide a greater rate of gas flow into the gap during wafer pick-up. Such an amount of flow may be desirable, for example, for use during pick-up of high surface area wafers, or for ultra-light wafer holders which could easily be lifted along with the wafer.

Hot Centering Wafer Support

With reference now to Figures 37 and 38, two wafer support structures 18g, 18h are depicted schematically, constructed in accordance with eighth and ninth embodiments of the present invention, respectively. Desirably, the wafer support structures 18g, 18h comprise the same preferred materials as in the previous embodiments, namely SiC for the wafer holders 20g, 20h and quartz for the respective spiders 22b, 22h. The wafer holders 20g are also preferably of low thermal mass, as described above.

Numerous features are substantially the same as corresponding features of the previous embodiment, such that like reference numerals will be used to describe like features. For example, the spider of Figure 38 can be identical to that illustrated in Figure 26, such that the reference numeral 22b is employed. Similar but significantly altered features will be referred to with like reference numerals with the additional suffixes "g" and "h." Additionally, new features without analogous features in the first embodiment will also carry the suffixes "g" and "h."

Like the second preferred embodiment (Figures 26-27), each of the embodiments of Figures 38 and 39 feature mechanisms which are self-centering at high temperatures. The principal differences between the second embodiment and Figures 38-39 are: the eighth and ninth embodiments do not include cold centering features; the base plates 50g, 50h and ring portions 52g, 52h are integral or one piece; the ring portions 52g, 52h are substantially the same thickness as their corresponding base plates 50g, 50h; and the wafer holders 20g, 20h do

not include peg recesses to aid rotational coupling to the corresponding spiders 22b, 22h. Each of these features, which are common to both eighth and ninth embodiments, are individually and synergistically advantageous from thermal and manufacturing viewpoints.

With reference initially to Figure 38, as noted, the wafer holder 20g of the eight embodiment is preferably one piece and the base plate 50g and ring portion 52g have substantially uniform thickness. As with the previous embodiments, the base plate thickness is close to that of the wafer it is to hold (*e.g.*, about 0.0285 inch for a 200 mm wafer or about 0.0305 inch for a 300 mm wafer). A transition portion 280g connects the integral base plate 50g and ring portion 52g. The transition portion 280g is an annular step which defines the wafer pocket. A plurality of lips 66g (preferably six), similar to the lips of previously described embodiments, extend integrally from the base plate 50g, defining a gap between the supported wafer 16 and the top surface 56g of the base plate 50g. To form the wafer pocket with the appropriate gap, the transition portion 280g has a vertical thickness of about 0.077 inch (for the 200 mm wafer support), forming a vertical transition inner wall 54g (also defining an inner edge of the ring portion 52g) and a vertical transition outer wall 229g (also defining an outer edge of the base plate 50g).

The base plate 50g is supported by the spider 22b, and particularly by three spider posts 64b. The tip of each post 64b includes a platform 240b on the inner side of the post 64b. The base plate 50g of the wafer holder 20g rests upon the platform 240b. In operation, the rotation of the spider 22b is transferred or coupled to the wafer holder 20g by friction between the spider post platform 240g and the base plate lower surface.

Each of the posts 64b also include an upward extension or protrusion with a hot centering surface. In particular, the post protrusion comprises the centering peg 242b having an inner hot centering surface 244b. As with the embodiment of Figure 26, the hot centering surface 244b is desirably proximate to or contacts an outer vertical wall of the wafer holder 20g (*e.g.*, the transition outer wall 229g) when the system is at high temperature (*e.g.*, at about 1200°C), due to differential expansion of the base plate 50g (preferably SiC) relative to the spider 22b (preferably quartz). "Proximate to," within the meaning of the present disclosure, means that the centering surface of the spider 22b should be within about 0.020 inch of a vertical wafer holder surface, and more preferably within about 0.010 inch. It will be understood that, because at least three such hot centering surfaces 244b on at least three different spider posts 64b are distributed about the periphery of the base plate 50g, the base plate 50g is confined to negligible movement.

Advantageously, the embodiment of Figure 38 includes many of the advantages of the previously disclosed embodiments. For example, the wafer holder 20g is self-centering at high temperatures, when thermal uniformity is most difficult to achieve, and most important to achieve. Additionally, rotational coupling also promotes uniform thermal effects. The illustrated embodiments, however, accomplish both hot centering and rotational coupling with fewer sharp internal corners (such as those created by the peg recesses shown in Figures 26 and 27). Such sharp corners tend to concentrate thermal, electrical and physical stresses. Naturally, reduction of corners also simplifies manufacturing.

Cross-sectional (thickness) uniformity of the ring 52g and base plate 50g also enables uniform temperatures across the wafer holder 20g. Essentially, the rate at which temperature increases during heating is proportional to

the area receiving radiation divided by the thickness of the wafer holder 20g. Temperature non-uniformities, during certain thermal processes, can cause large enough stress to result in breakage of the wafer holder. For such processes, uniform thickness to avoid breakage is more important than edge loss compensation by use of a thicker ring portion.

5 With reference now to Figure 39, the ninth preferred embodiment offers similar advantages. As with the embodiment of Figure 39, the wafer holder 20g includes substantially uniformly thick base plate 50h and ring 52h portions, which are also integrally formed as one piece. The transition portion 280h, however, is angled outwardly, rather than being completely vertical. Preferably, the transition 280h forms an angle with the ring 52h and base plate 50h of between about 30° and 60°, and is shown at about 45° in Figure 39. The transition portion 280h
10 is thus defined between an annular and angled transition inner wall 54h and an outer wall 229h.

The wafer holder 20h, and particularly the base plate 50h, rests on inner platforms 240h of the spider posts 64h, rotationally coupled to the spider 22h by friction. The post protrusion 242h includes an angled hot centering surface 244h. At high temperatures, as the wafer holder 20h expands relative to the spider 22h. The holder's outer wall 229h mates with the hot centering surfaces 244h of each of the posts 64h (preferably at least
15 three), keeping the wafer holder 20h (and thus the wafer 16) centered within a tightly confined space.

The embodiment of Figure 39 entails similar advantages as those of Figure 38. Additionally, the angled transition portion 280h reduces the effective thickness of the transition region. For example, comparing equivalent wafer holders of the eighth and ninth embodiments, the vertical transition portion 280g of Figure 38 has a thickness of about 0.077", while the angled transition portion 280h of Figure 39 has an effective vertical thickness of about
20 0.040". Accordingly, the ninth embodiment experiences even fewer thermal non-uniformities during thermal processing, resulting in reduced breakage. Furthermore, the angle reduces the sharpness of corners and attendant stresses.

It will be appreciated by those skilled in the art that various modifications and changes may be made without departing from the scope of the invention, and all such modifications and changes are intended to fall within
25 the scope of the invention, as defined by the appended claims.

WE CLAIM:

1. A low mass wafer holder for supporting a substrate within a process reactor, the holder having an upper surface and a lower surface, the holder comprising a plurality of lips integral with the upper surface, the lips extending upwardly a uniform height above the upper surface to support the substrate with a uniform gap
5 between substrate and the upper surface.
2. The wafer holder of Claim 1, wherein the wafer holder consists essentially of silicon carbide.
3. The wafer holder of Claim 1, further comprising a base plate having a top surface and an annular ring having an inner wall, the ring inner wall extending above the wafer holder upper surface and defining a substrate
10 pocket with a diameter greater than the diameter of the substrate.
4. The wafer holder of Claim 3, wherein the ring is integrally formed with the base plate.
5. The wafer holder of Claim 3, wherein the ring comprises a shelf positioned below and radially inward of the ring inner wall, the wafer holder upper surface comprising the base plate top surface and a ring top
15 surface.
6. The wafer holder of Claim 5, wherein the lips are integrally formed with the ring shelf.
7. The wafer holder of Claim 3, wherein each of the lips comprises an inner face, a lip base wider than the inner face, and a pair of lip side walls extending between the inner face and the lip base.
8. The wafer holder of Claim 7, wherein the lip base is integral with the ring inner wall.
9. The wafer holder of Claim 1, wherein the wafer holder has a thermal mass less than about three
20 times a thermal mass of the substrate.
10. The wafer holder of Claim 9, wherein the thermal mass of the wafer holder is between about 0.5 and 2.0 times the thermal mass of the substrate.
11. The wafer holder of Claim 1, wherein each of the lips extends a height between about 0.010 inch and 0.030 inch above the upper surface, and the substrate comprises a 200 mm silicon wafer.
12. The wafer holder of Claim 1, wherein each of the lips extends a height between about 0.020 inch
25 and 0.050 inch above the upper surface, and the substrate comprises a 300 mm silicon wafer.
13. A wafer holder for supporting a substrate within a process reactor, the wafer holder comprising a central portion having a first thickness and an upper surface, a plurality of spacers projecting upwardly a uniform height above the upper surface and distributed to peripherally support the substrate above the upper surface, and a fringe portion having a second thickness, the fringe portion comprising a ring inner wall extending upwardly from
30 and surrounding the upper surface.
14. The wafer holder of Claim 13, wherein the wafer holder consists essentially of silicon carbide.
15. The wafer holder of Claim 13, wherein the wafer holder has a thermal mass less than about five times a thermal mass of the substrate.
16. The wafer holder of Claim 13, wherein the spacers are arranged in a circular pattern and the
35 substrate comprises a generally circular semiconductor wafer.

17. The wafer holder of Claim 16, wherein 3 to 12 spacers are distributed with radial symmetry about the upper surface.

18. The wafer holder of Claim 16, wherein the semiconductor wafer comprises a 300 mm silicon wafer, greater than about 9 spacers are distributed with radial symmetry about the upper surface, and further comprising at least one central supporting member.

19. The wafer holder of Claim 16, wherein each of the spacers are lips comprising an inner face, an outer base, and two side walls connecting the inner face to the outer base, the inner faces defining a first circle having a radius smaller than the radius of the wafer.

20. The wafer holder of Claim 19, wherein the radius of the first circle is smaller than the radius of the wafer by less than about 3 mm.

21. The wafer holder of Claim 19, wherein the radius of the first circle is smaller than the radius of the wafer by about 2 mm.

22. The wafer holder of Claim 19, wherein the outer bases define a second circle of radius greater than the radius of the wafer.

23. The wafer holder of Claim 13, wherein the holder comprises a base plate and an independent ring.

24. The wafer holder of Claim 23, wherein the ring comprises an annular band, including the ring inner wall, and an annular shelf extending radially inward of the band, such that the central portion comprises the base plate and the shelf of the ring, and the fringe portion comprises the band.

25. The wafer holder of Claim 24, wherein the spacers are integrally formed with the shelf.

26. The wafer holder of Claim 23, wherein the ring supports the base plate.

27. The wafer holder of Claim 23, wherein the base plate supports the ring.

28. The wafer holder of Claim 13, wherein the second thickness is between about 1.2 times and 3.0 times greater than the first thickness.

29. A semiconductor reactor for treating a substrate, comprising:
a reaction chamber;
a plurality of heat sources;
a self-centering single-wafer support structure, having a first level and centered position at a first temperature and a second level and centered position at a second temperature, including:

a wafer holder for directly supporting the substrate, having a first coefficient of thermal expansion, and the wafer holder comprising at least one recess in a bottom surface thereof, and
a support spider for supporting the wafer holder, having a second coefficient of thermal expansion different from the first coefficient, the spider comprising at least three support posts cooperating with the recess of the wafer holder.

30. The reactor of Claim 29, wherein the wafer holder consists essentially of silicon carbide.

31. The reactor of Claim 29, wherein the recess comprises at least three radial grooves corresponding to the support posts of the spider, distributed at even angular distances from one another about the wafer holder bottom surface.

5 32. The reactor of Claim 31, wherein each of the radial grooves comprises a pair of radially oriented flat surfaces defining a groove opening, each of the support posts including a curved end sized to fit within the groove opening.

33. The reactor of Claim 32, wherein the grooves are distributed equidistantly from a wafer holder center.

10 34. The reactor of Claim 32, wherein the support spider comprises three posts connected to and equidistant from a central spider hub.

35. The reactor of Claim 34, wherein the spider hub is connected to a rotatable shaft.

36. The reactor of Claim 29, wherein the wafer holder comprises a disk-shaped base plate and an independent annular ring.

15 37. The reactor of Claim 36, wherein the ring supports the base plate and the recess comprises at least three radial grooves formed in an undersurface of the ring.

38. The reactor of Claim 36, wherein the base plate supports the ring and the recess is formed between a base plate outer wall and a ring inner wall, and each of the support posts comprises an upwardly protruding peg having a radially outer surface and a radially inner surface, the outer peg surface of each support post positioned proximate the ring inner wall at the first temperature, the inner peg surface of each support post positioned proximate the base plate outer wall at the second temperature.

39. The reactor of Claim 38, wherein the first temperature is lower than the second temperature.

40. The reactor of Claim 38, wherein the recess comprises an annular groove defined by a base plate peripheral edge, a vertical groove inner wall, and an annular ring ledge extending radially inward from the groove inner wall.

25 41. The reactor of Claim 40, wherein the recess further comprises at least three peg recesses corresponding to the support posts, the peg recesses extending radially outward from the annular groove, the ring inner wall comprising a peg recess back wall and the base plate outer wall comprising the base plate peripheral edge.

42. A low mass wafer holder for supporting a single substrate in a processing chamber, comprising:
30 a disc-shaped base plate;
an annular ring independent of the base plate;
an annular hanging portion integral with one of the ring and the base plate; and
an annular supporting portion integral with the other of the ring and the base plate, the supporting portion underlying and supporting the hanging portion.

43. The low mass wafer holder of Claim 42, wherein the wafer holder has a thermal mass less than
35 about three times a thermal mass of the substrate.

44. The low mass wafer holder of Claim 43, wherein the thermal mass of the wafer holder is between about 0.5 and 2.0 times the thermal mass of the substrate.
45. The low mass wafer holder of Claim 42, wherein the wafer holder consists essentially of silicon carbide.
- 5 46. The low mass wafer holder of Claim 42, wherein the hanging portion comprises an overhang of the base plate, extending radially outward from a central portion of the base, and the supporting portion comprises a step of the ring, extending radially inward from a outer portion of the ring.
47. The low mass wafer holder of Claim 46, wherein the outer portion of the ring comprises an annular shelf extending radially outward from the step and having a thickness equal to that of the central portion
10 of the base plate, the thickness of the base overhang and the ring step selected such that an upper surface of the base plate is flush with an upper surface of the shelf and a lower surface of the base plate is flush with a lower surface of the ring.
48. The low mass wafer holder of Claim 46, wherein the step of the ring includes a plurality of bumps defining an annular backside passage between the step and the overhang of the base plate.
- 15 49. The low mass wafer holder of Claim 42, wherein the hanging portion comprises an overhanging ledge of the ring, extending radially inward from an outer portion of the ring, and the supporting portion comprises a peripheral portion of the base plate.
50. The low mass wafer holder of Claim 42, further comprising a plurality of spacers supporting the substrate at a uniform height above the base plate.
- 20 51. A semiconductor reactor, comprising:
a reaction chamber;
a plurality of heat sources;
a wafer support structure for supporting a wafer, including a low mass wafer holder; and
a temperature sensor driving at least one of the heat sources, the temperature sensor sensing the
25 temperature at a point vertically spaced from the wafer holder.
52. The reactor of Claim 51, wherein maintaining the temperature sensor at about 1000°C and moving the temperature sensor about 0.010 inch relative to the wafer holder results in steady state wafer temperature difference of less than about 10°C.
53. The reactor of Claim 51, wherein the wafer support structure further comprises a support spider
30 having at least three support posts engaged with an undersurface of the wafer holder, the support posts connected to a central spider hub spaced below the wafer holder, the temperature sensor comprising a thermocouple extending through the spider hub.
54. The reactor of Claim 51, wherein the thermal mass of the wafer holder is between about 0.5 and 2.0 times the thermal mass of the wafer.
- 35 55. A low mass wafer holder for supporting a single substrate in a processing chamber, comprising an upper support surface including a plurality of open radial channels, each of the channels having a width less than

the thermal diffusion length in the substrate, a total volume of the channels sufficient to permit lifting the substrate independently from the wafer holder.

56. The low mass wafer holder of Claim 55, consisting essentially of silicon carbide.

57. The low mass wafer holder of Claim 55, wherein the plurality of channels comprises 32 channels
5 having a depth of about 0.010 inch and a width of about 0.015 inch.

58. The low mass wafer holder of Claim 68, wherein the thermal mass of the wafer holder is between about 0.5 and 2.0 times the thermal mass of the substrate.

59. A wafer holder for supporting a substrate, the wafer holder having a thermal mass less than about five times the thermal mass of the substrate, comprising:

10 a base plate extending generally parallel with and spaced below the substrate;
an annular lip for peripherally supporting the substrate above the base plate, the lip having an inner face defining a gap between the substrate and the base plate; and
a gas passage communicating from an underside of the wafer holder to the gap between the substrate and the base plate.

15 60. The wafer holder of Claim 72, consisting essentially of silicon carbide.

61. The wafer holder of Claim 72, wherein the annular lip is integral with and extends inwardly from a ring.

62. The wafer holder of Claim 74, wherein the ring supports the base plate.

63. The wafer holder of Claim 75, wherein the base plate is supported upon a plurality of bumps on
20 the ring, and the gas passage comprises an annular opening between the base plate and the ring.

64. The wafer holder of Claim 76, wherein the gas passage further comprises at least one conduit extending inwardly and upwardly at an angle through the wafer holder from a lower surface of the ring.

65. The wafer holder of Claim 72, wherein the gas passage comprises at least one conduit extending inwardly and upwardly at an angle through the annular lip.

25 66. The wafer holder of Claim 78, wherein the gas passage comprises a plurality of conduits, the openings of which are spaced to align with a plurality of hollow support posts of a support spider.

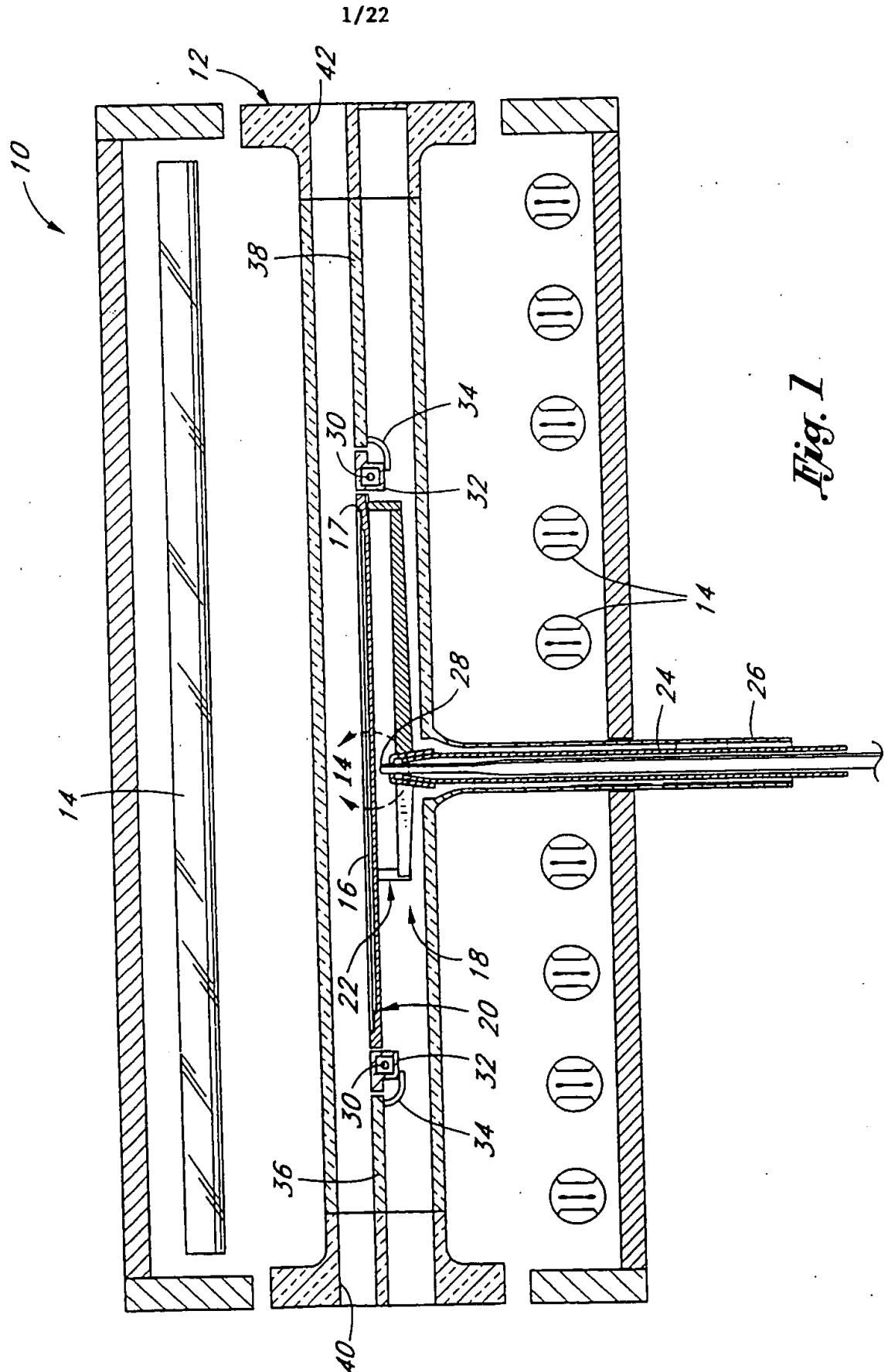
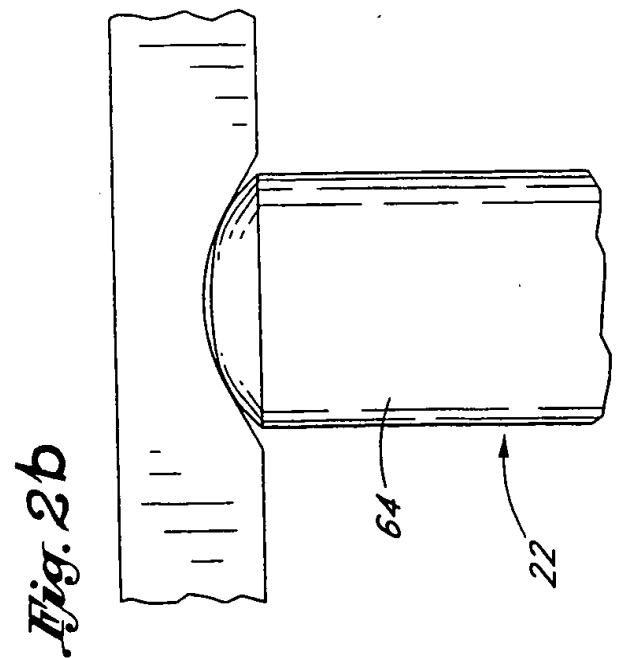
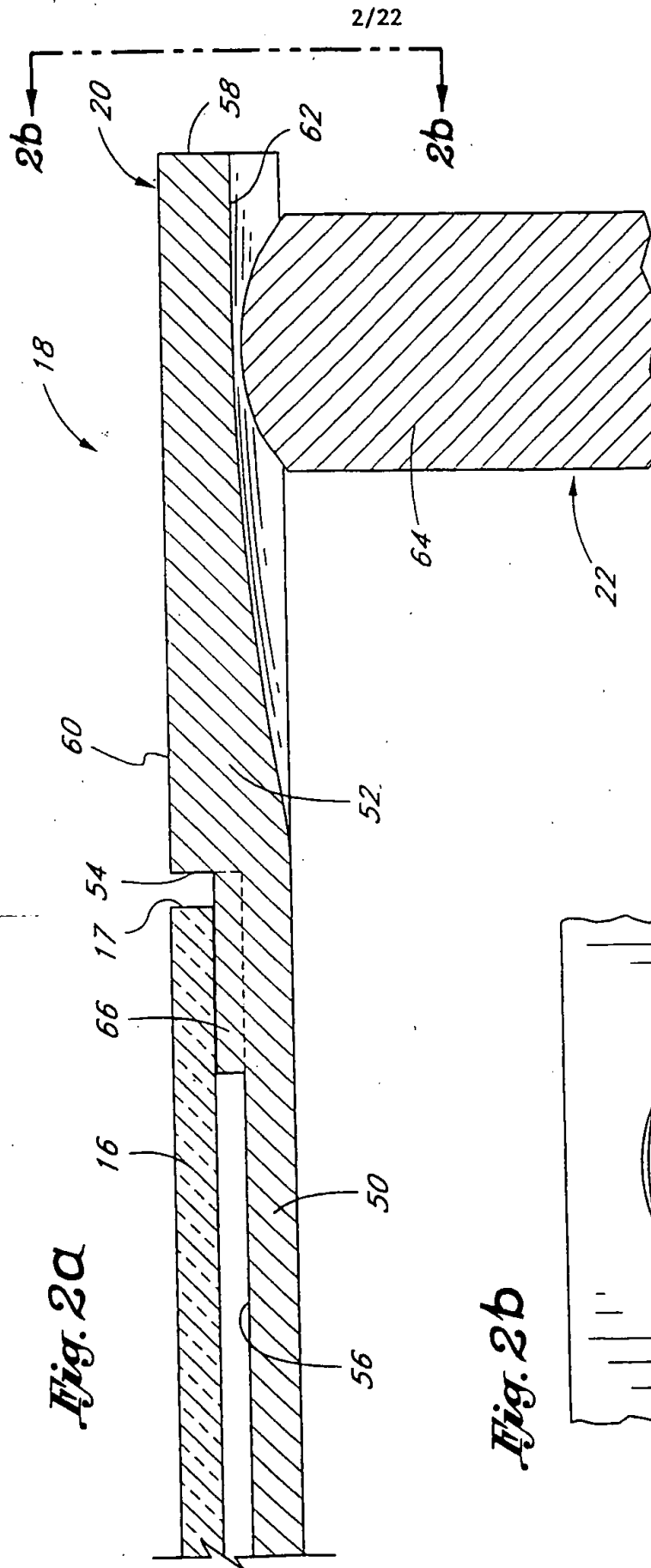


Fig. 1



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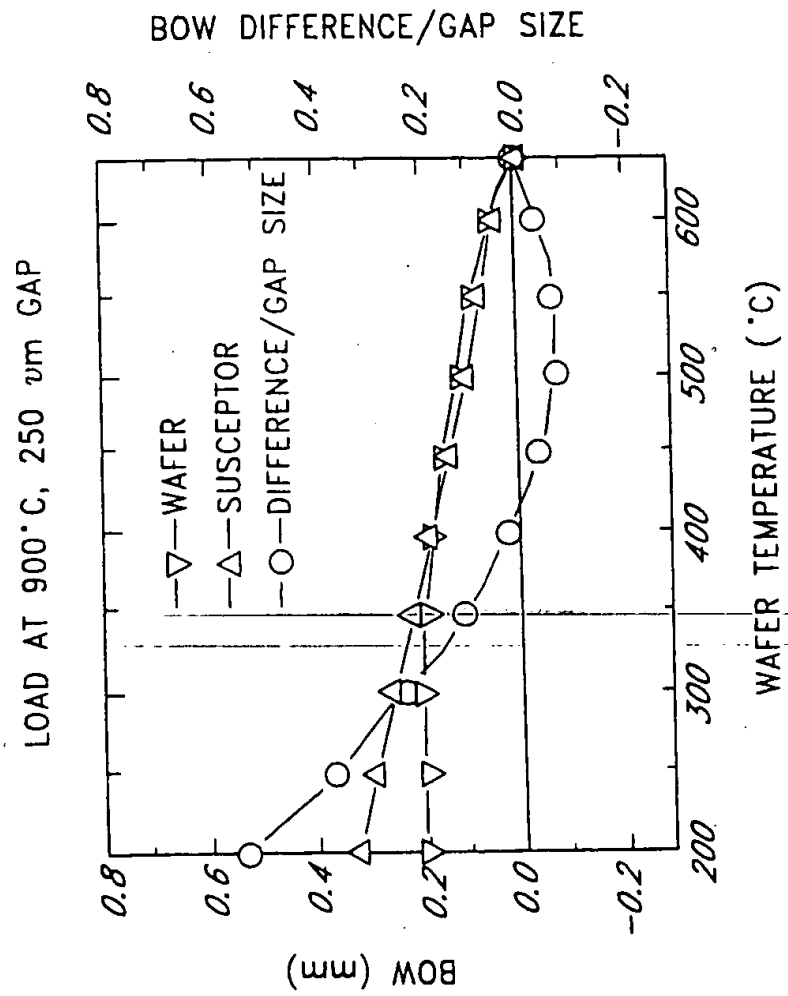


FIG. 3

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Fig. 4

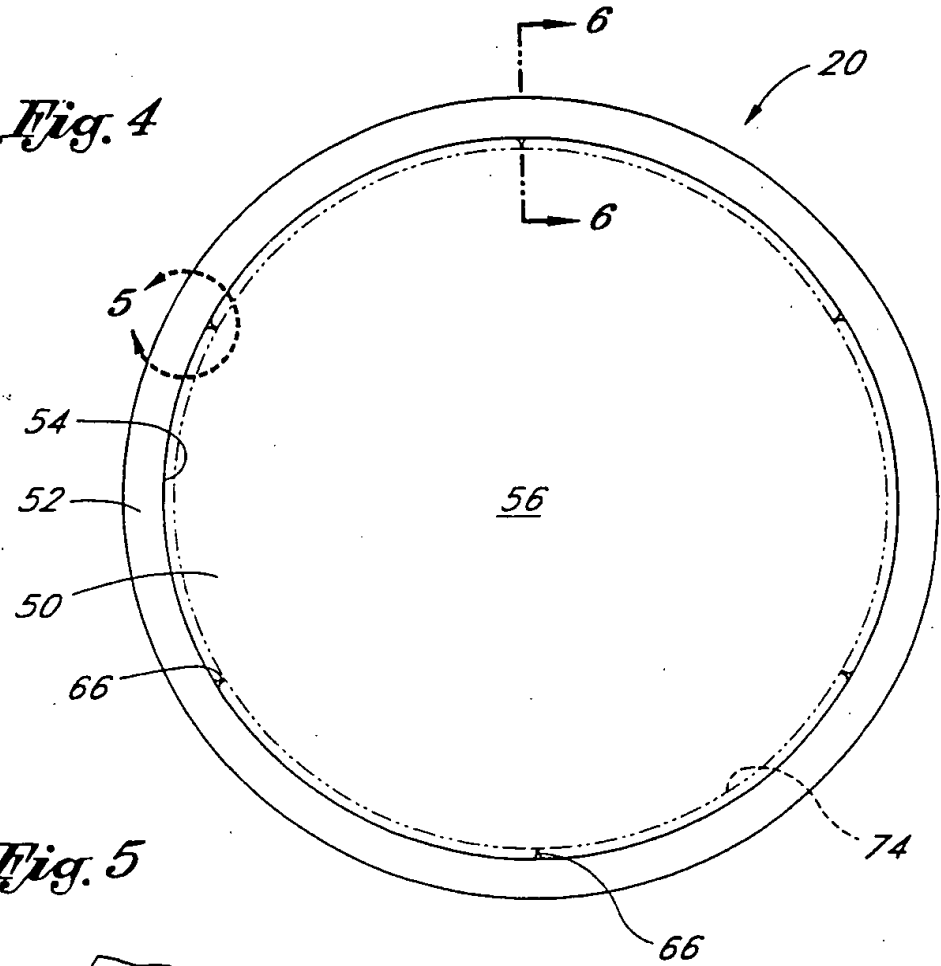


Fig. 5

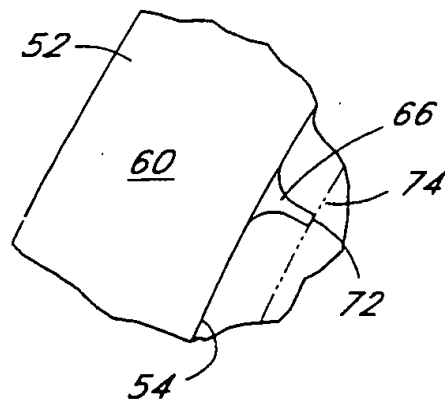


Fig. 7

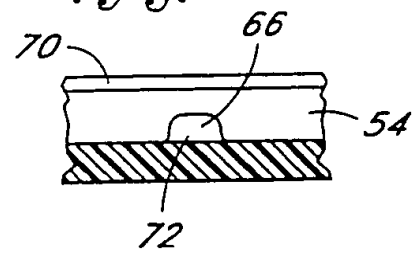
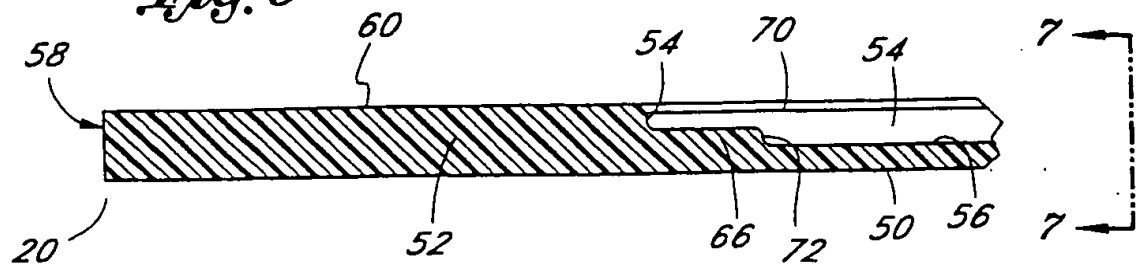


Fig. 6



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Fig. 8

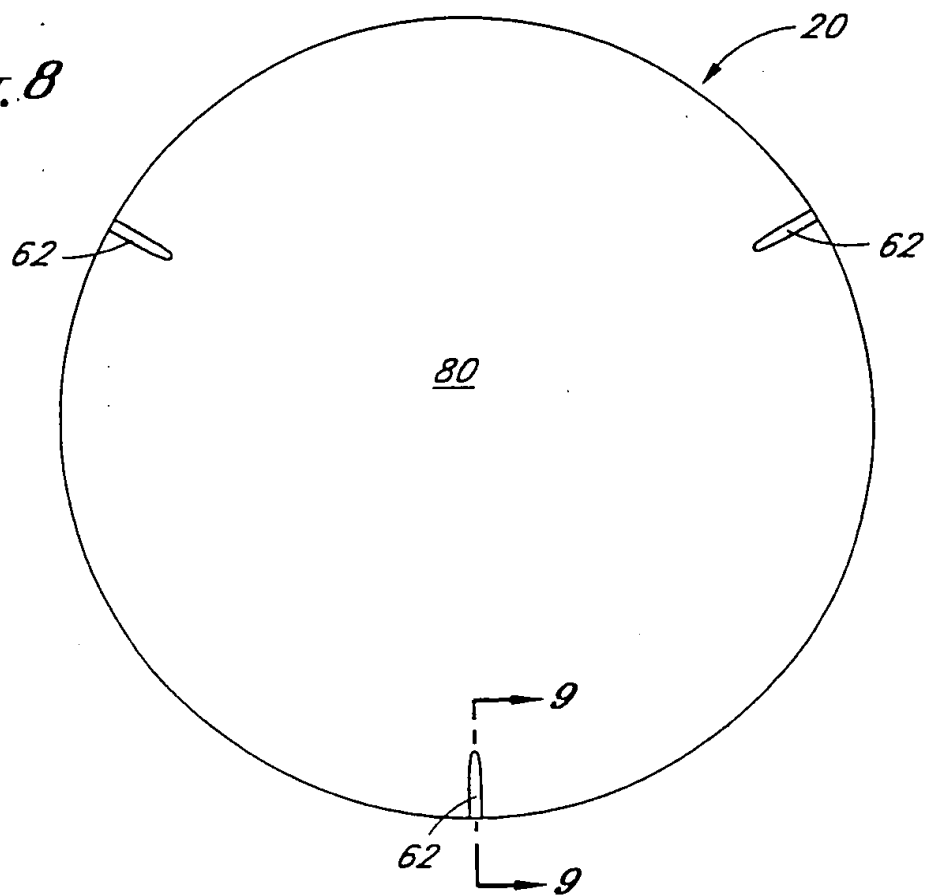


Fig. 9

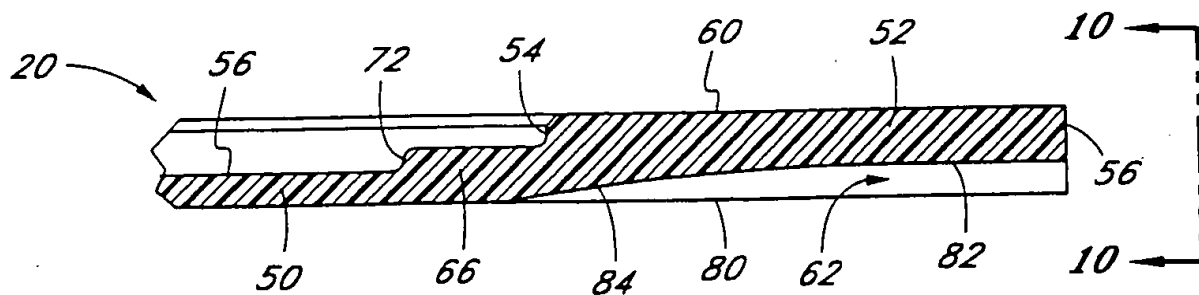
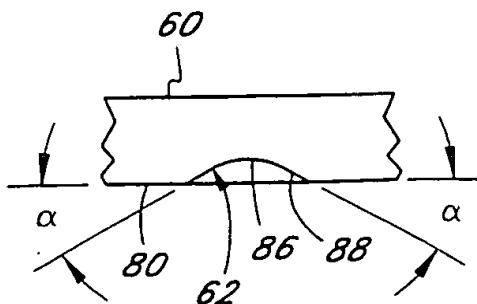


Fig. 10



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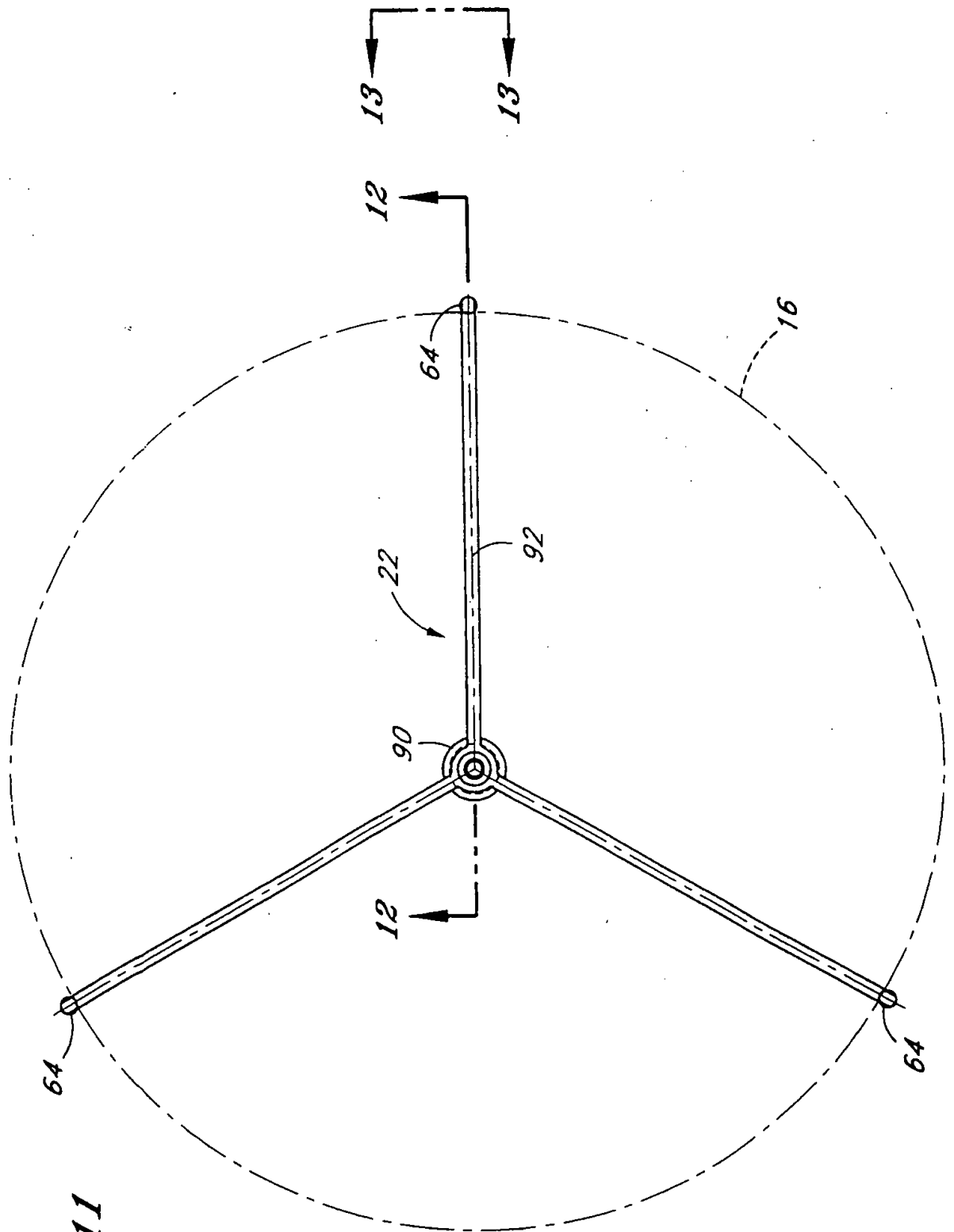
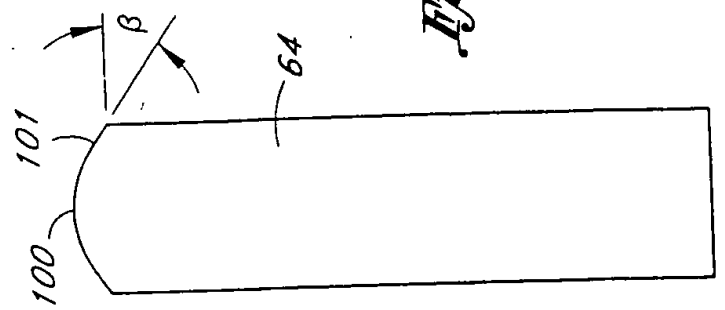
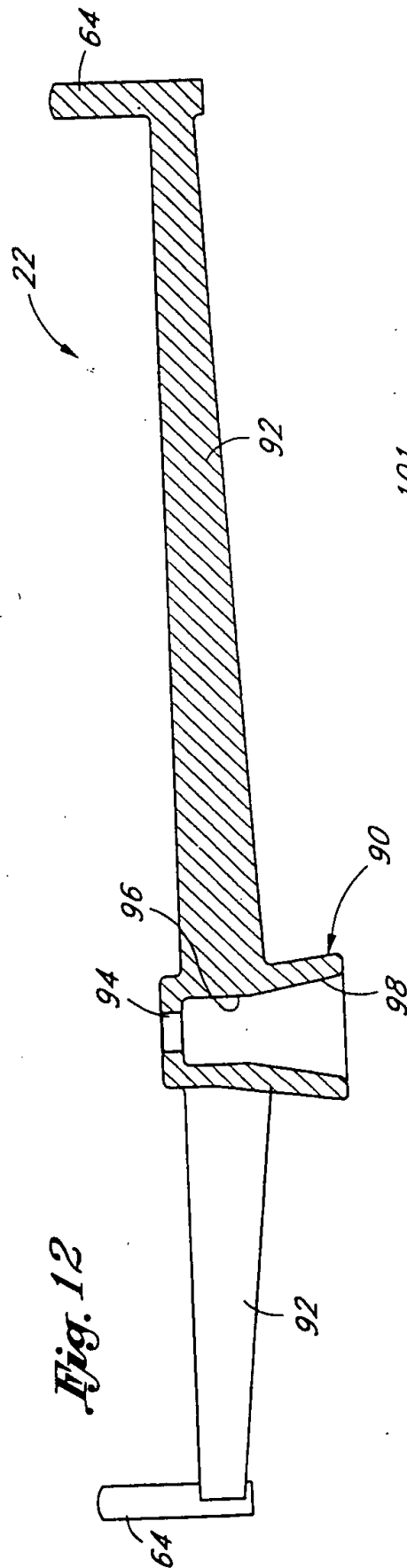
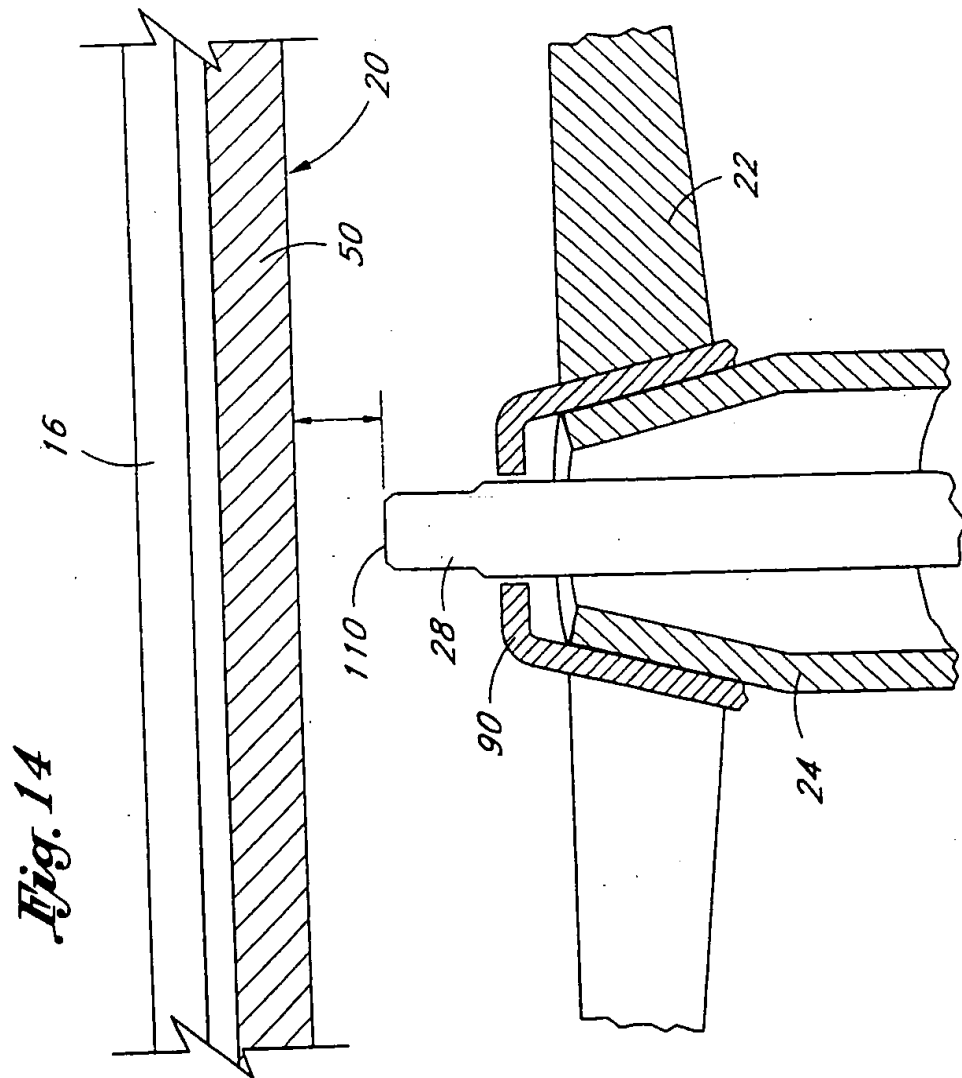


Fig. 11

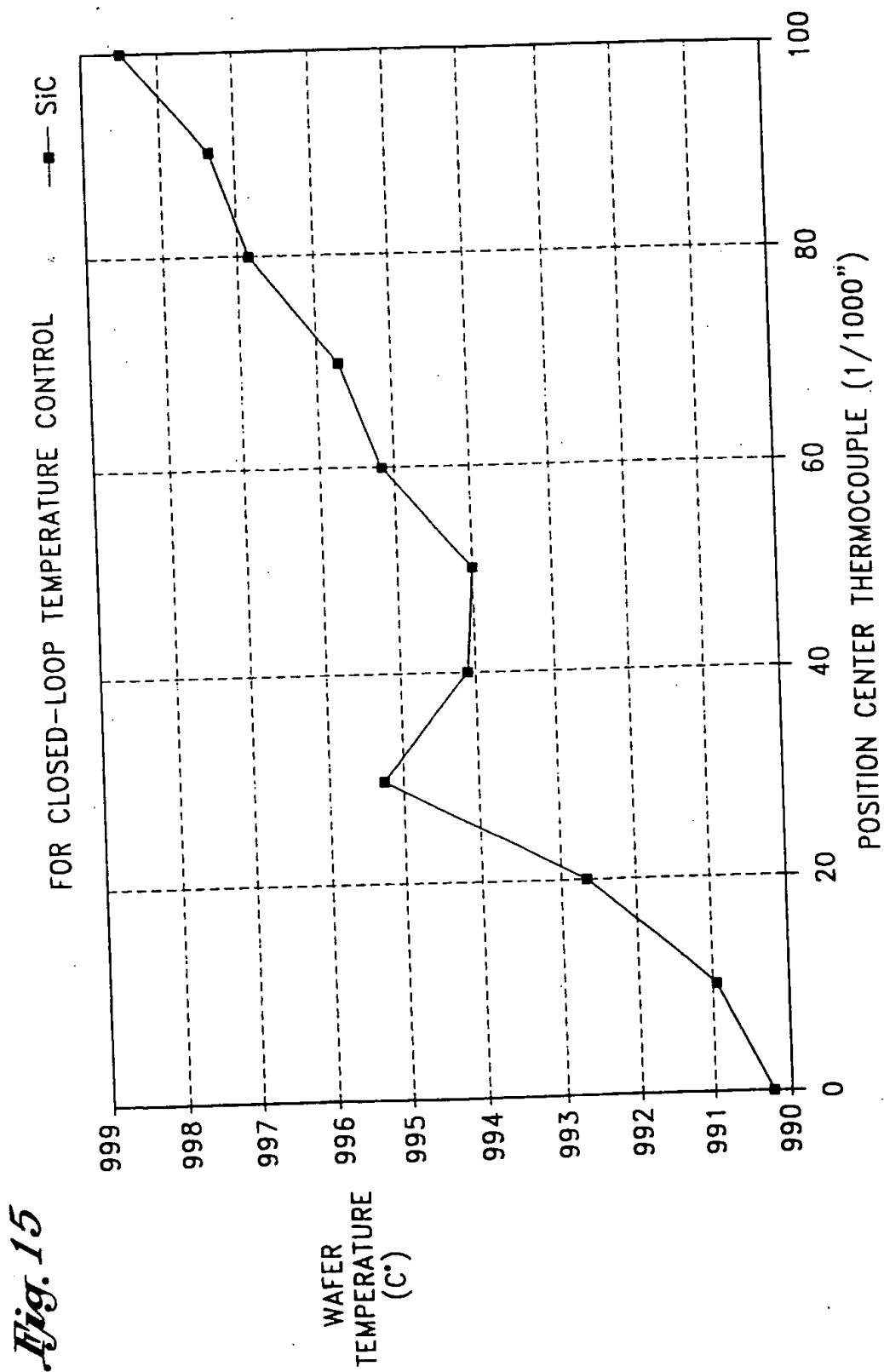
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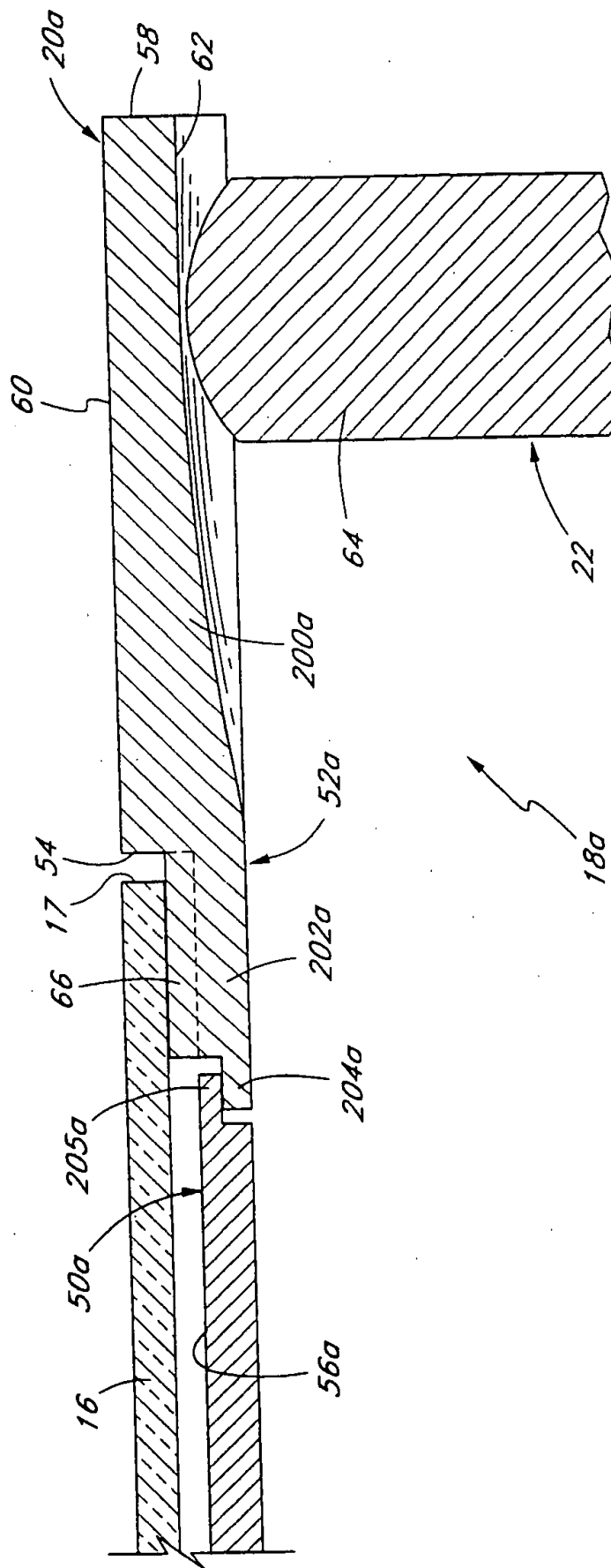


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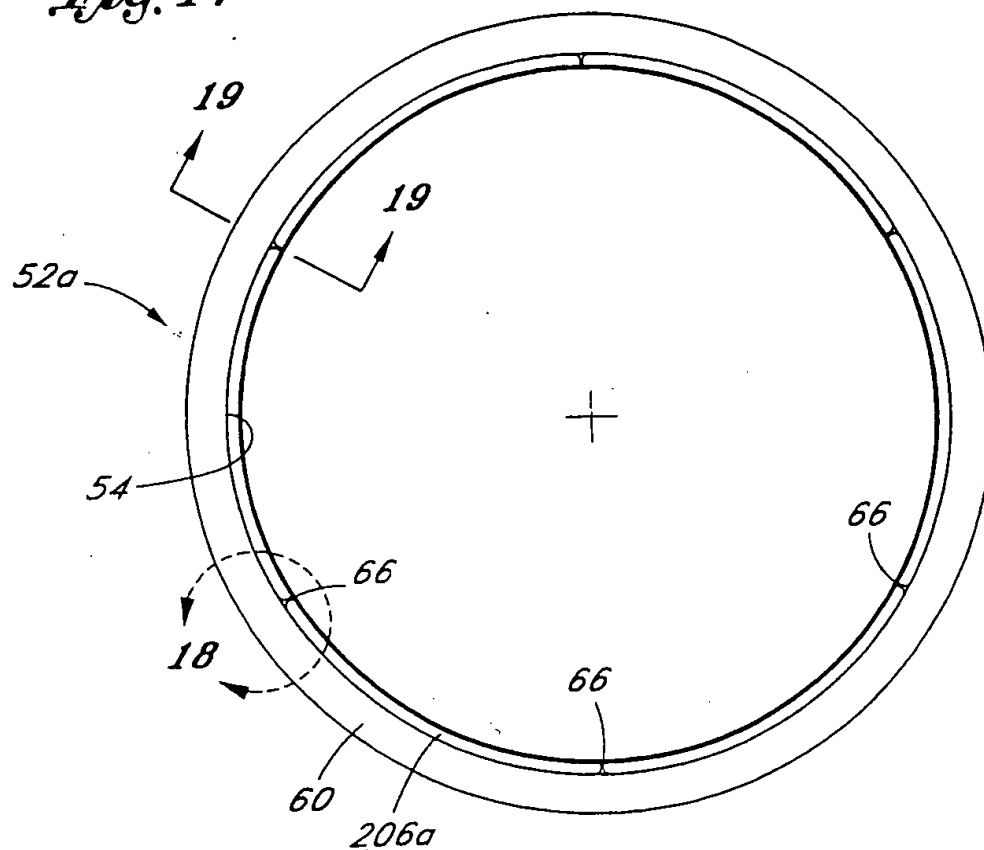
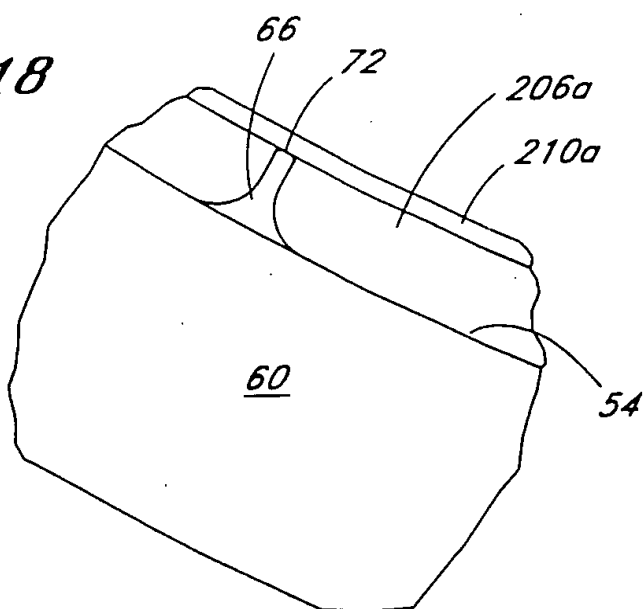


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Fig. 16



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Fig. 17*Fig. 18*

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Fig. 19

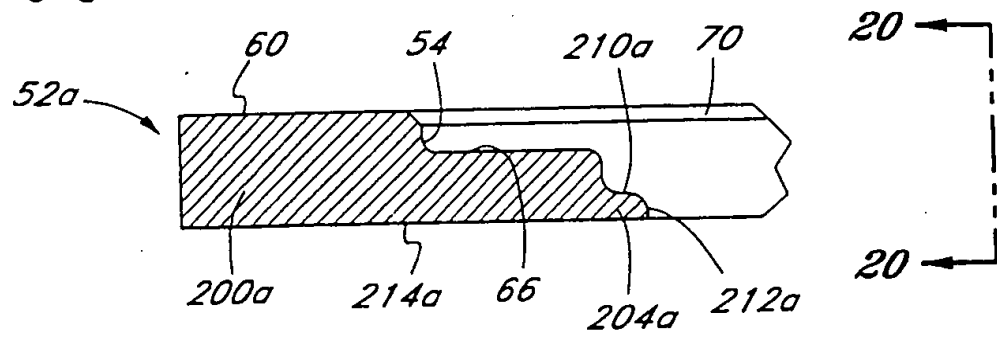


Fig. 20

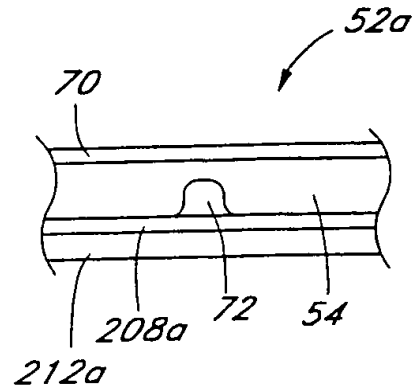
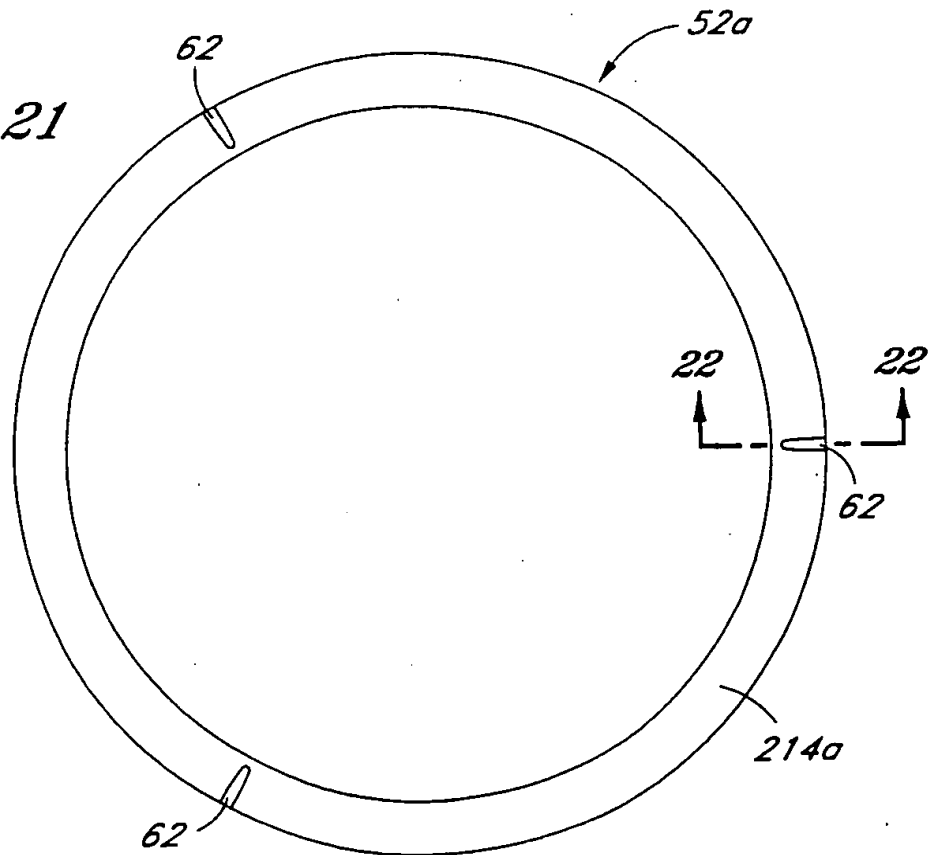
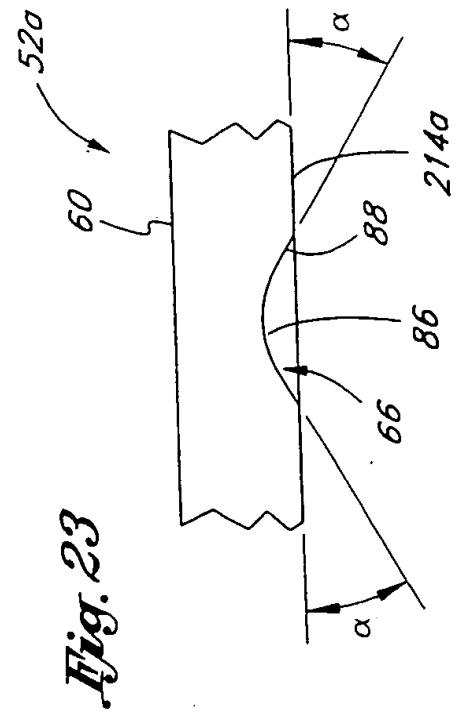
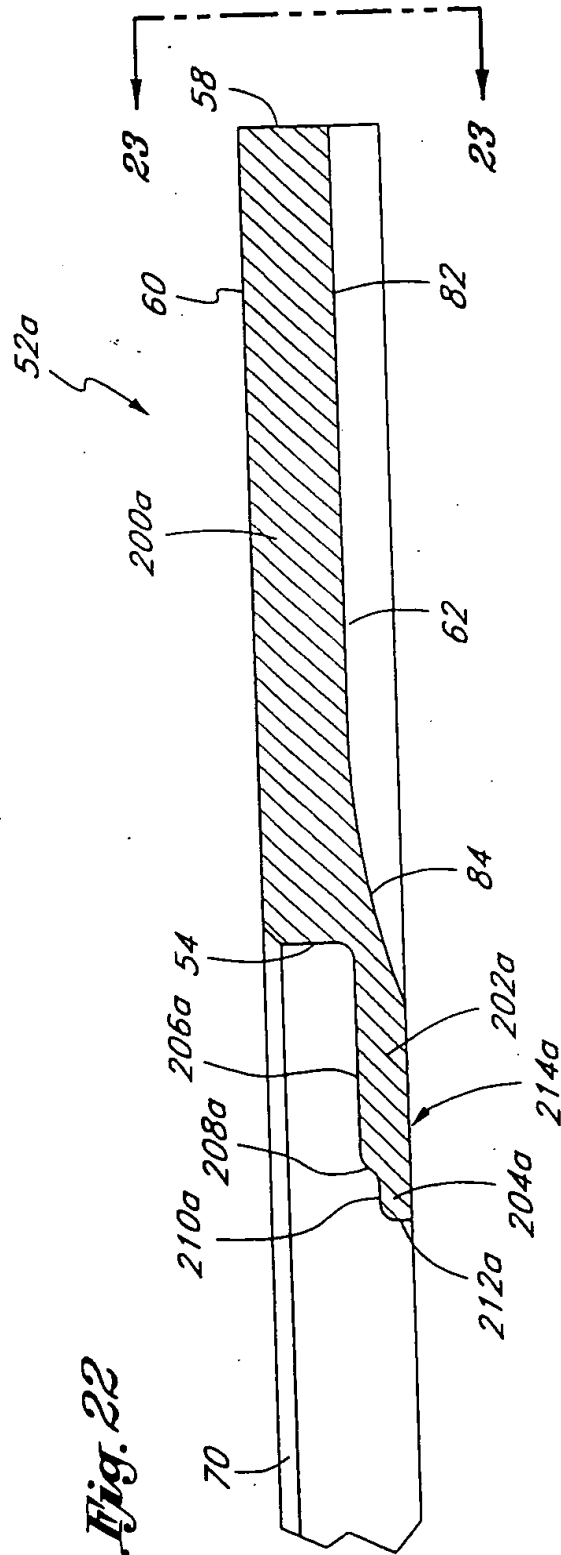


Fig. 21





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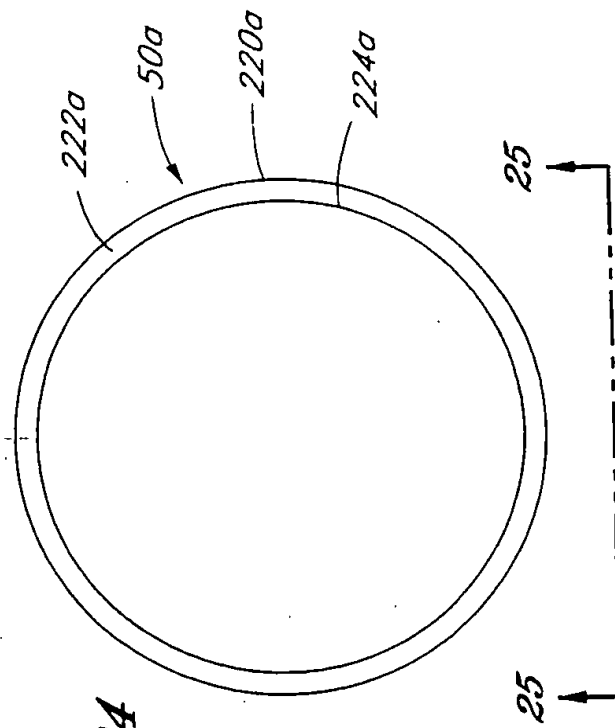


Fig. 24

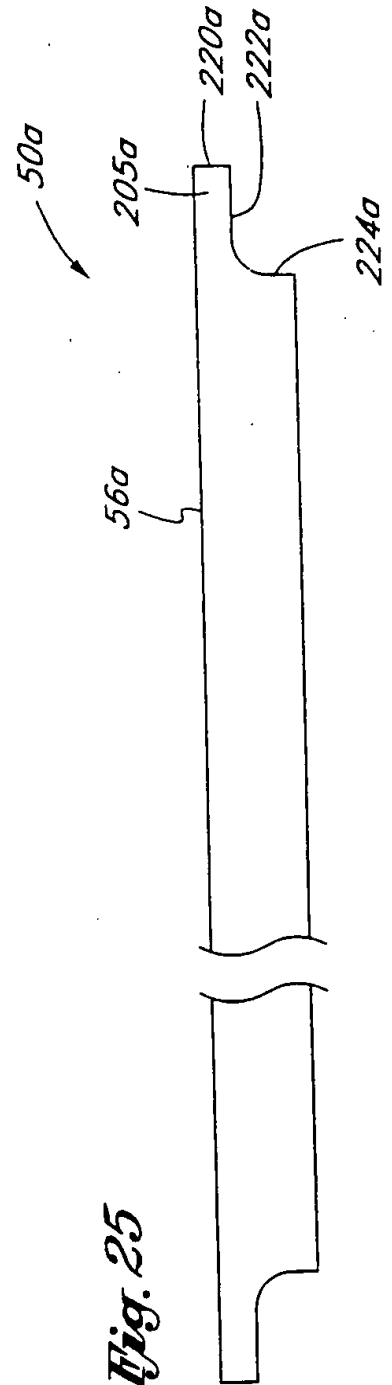
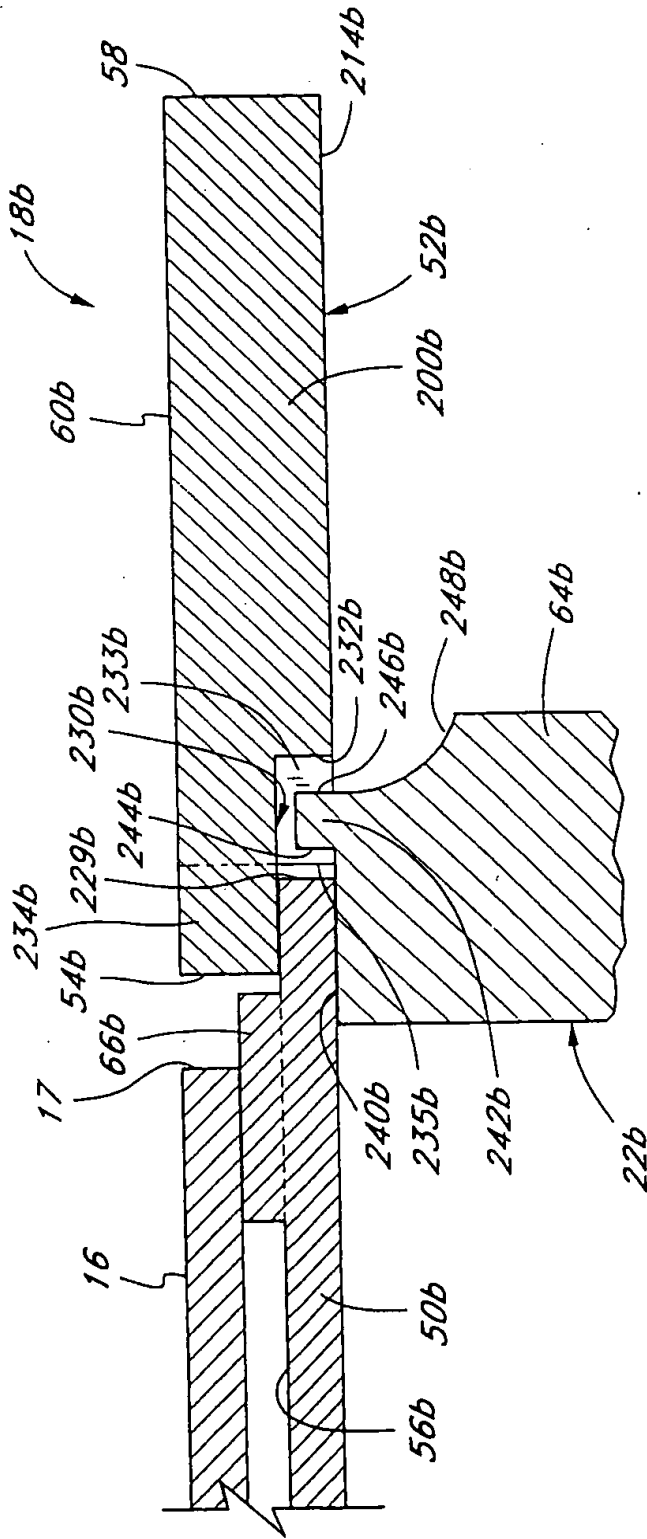


Fig. 25

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Fig. 26



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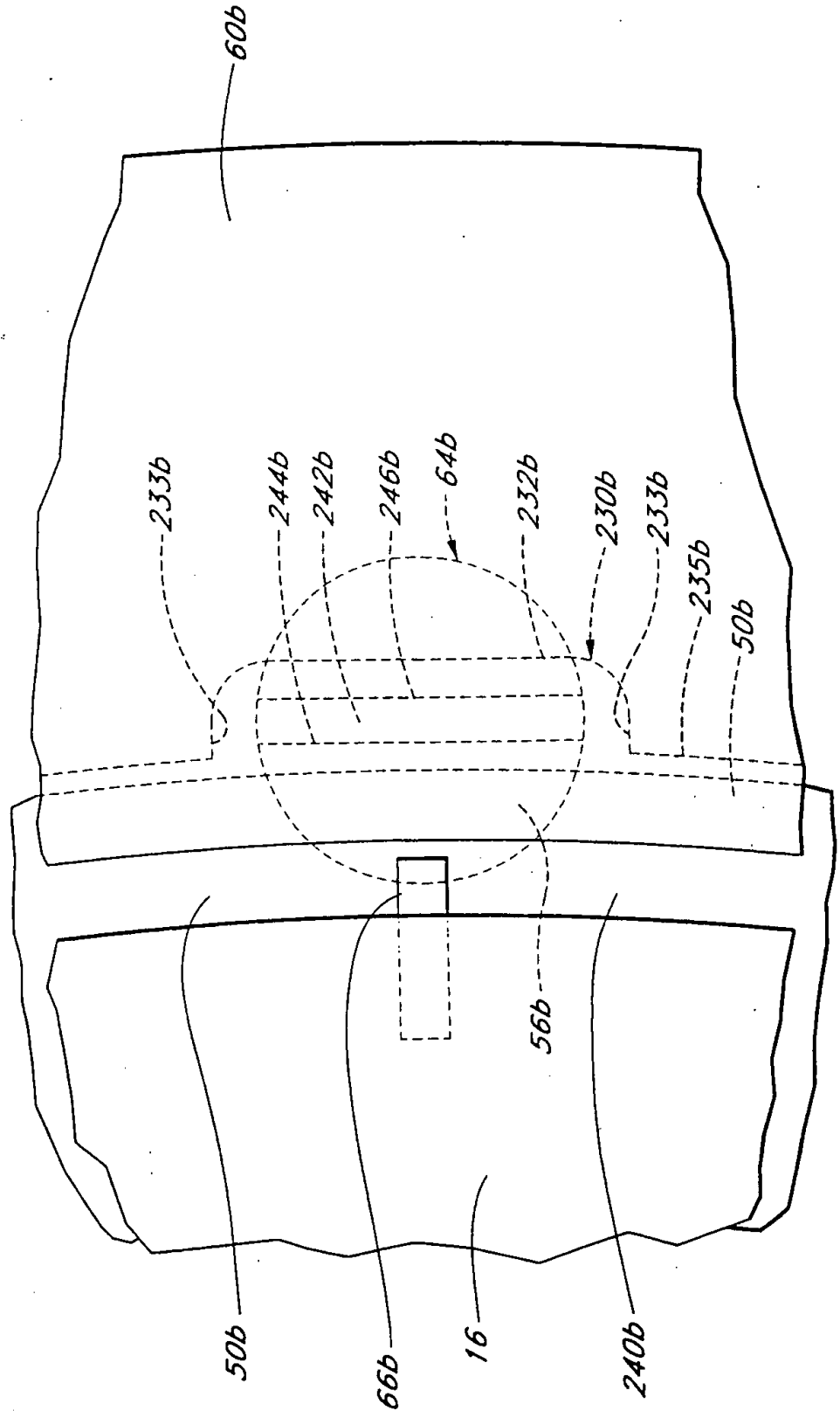


Fig. 27

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Fig. 28

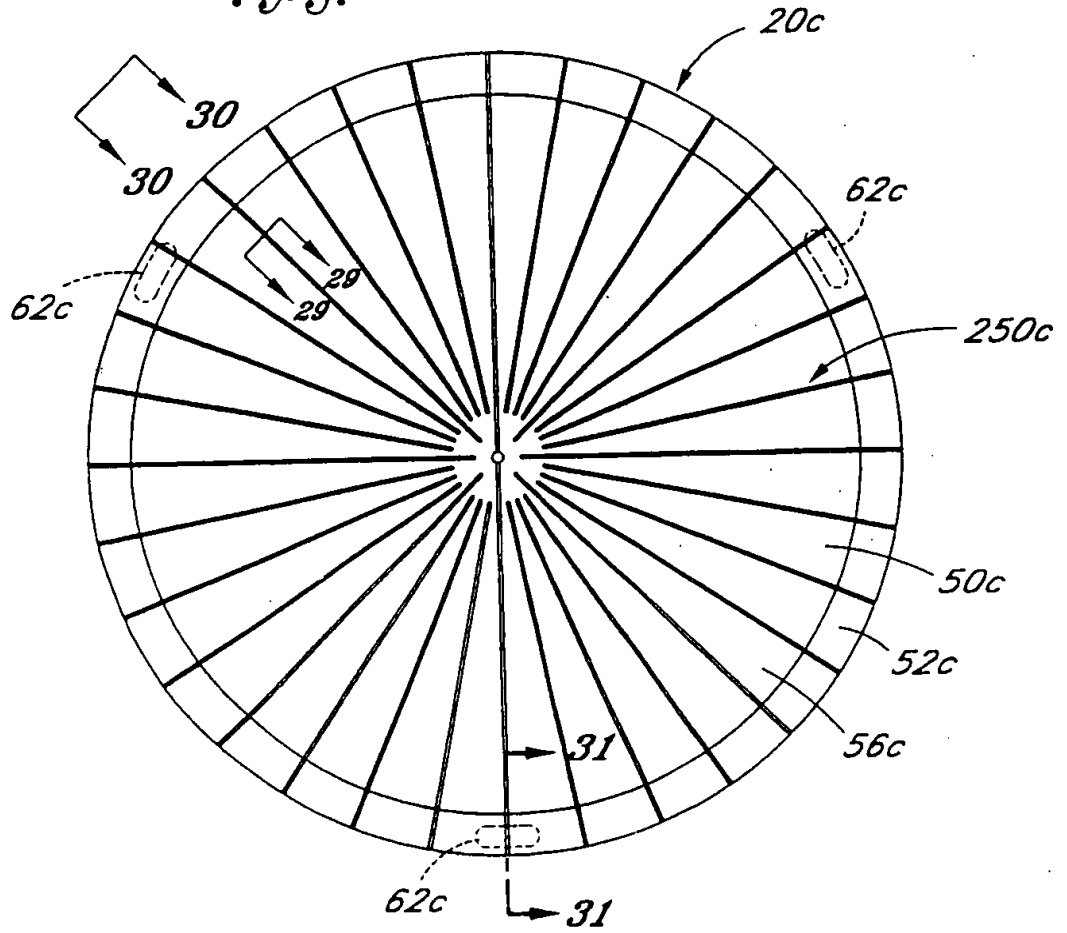


Fig. 29

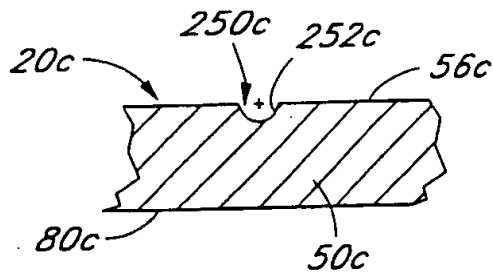


Fig. 30

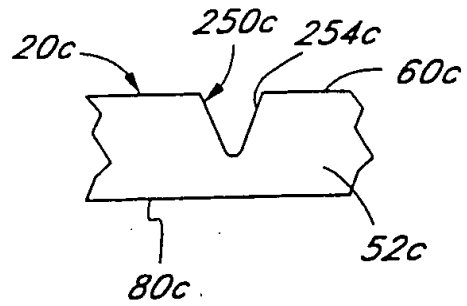
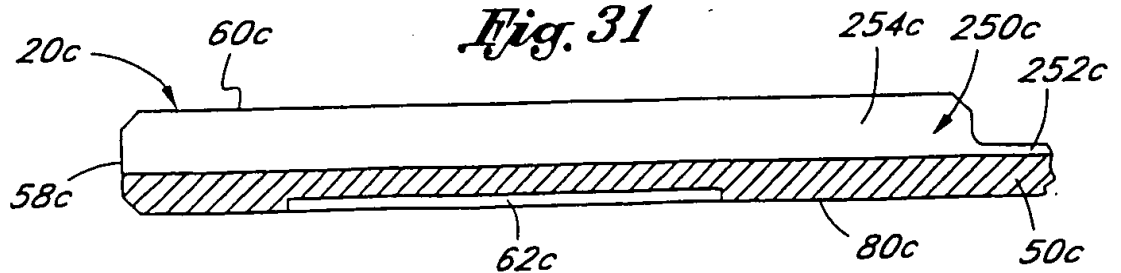
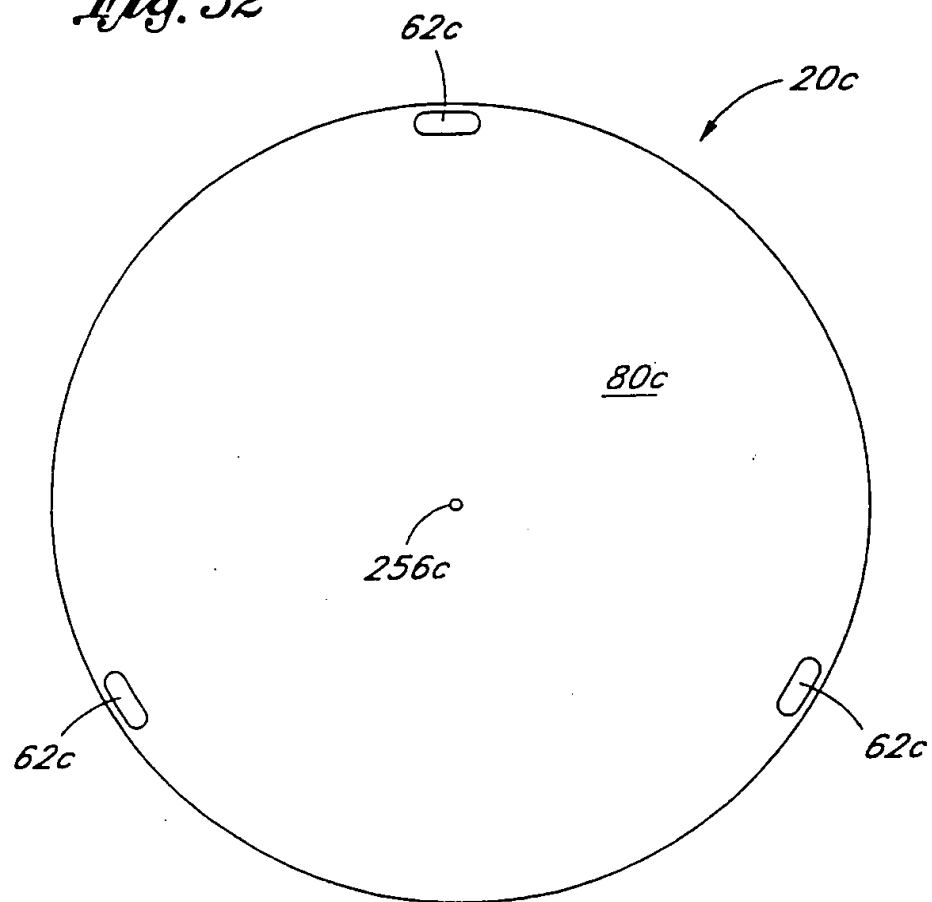


Fig. 31



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Fig. 32

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Fig. 33

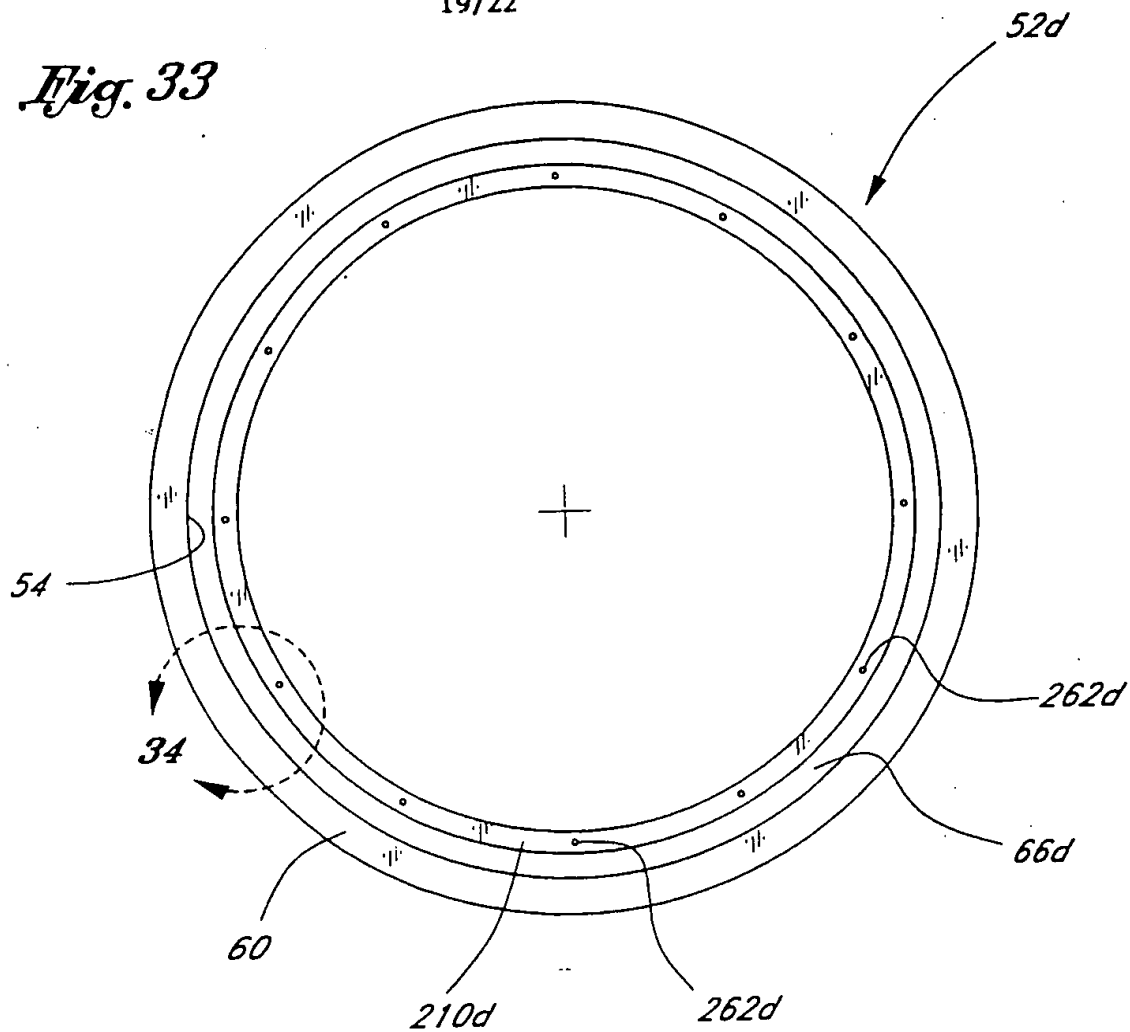
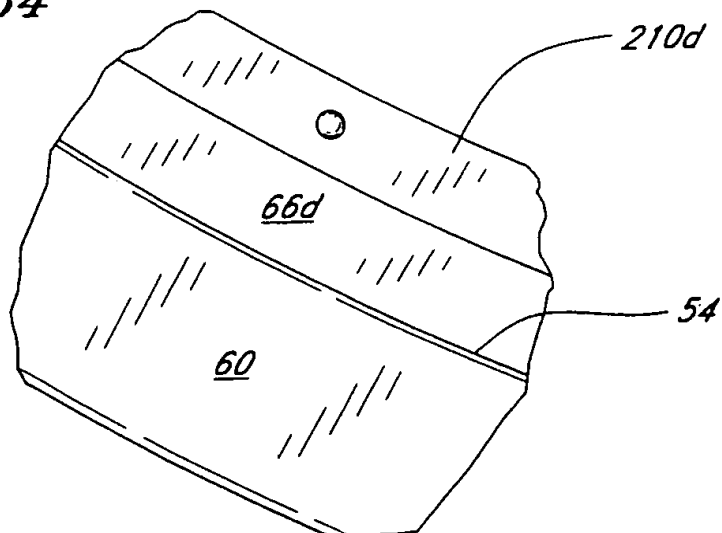
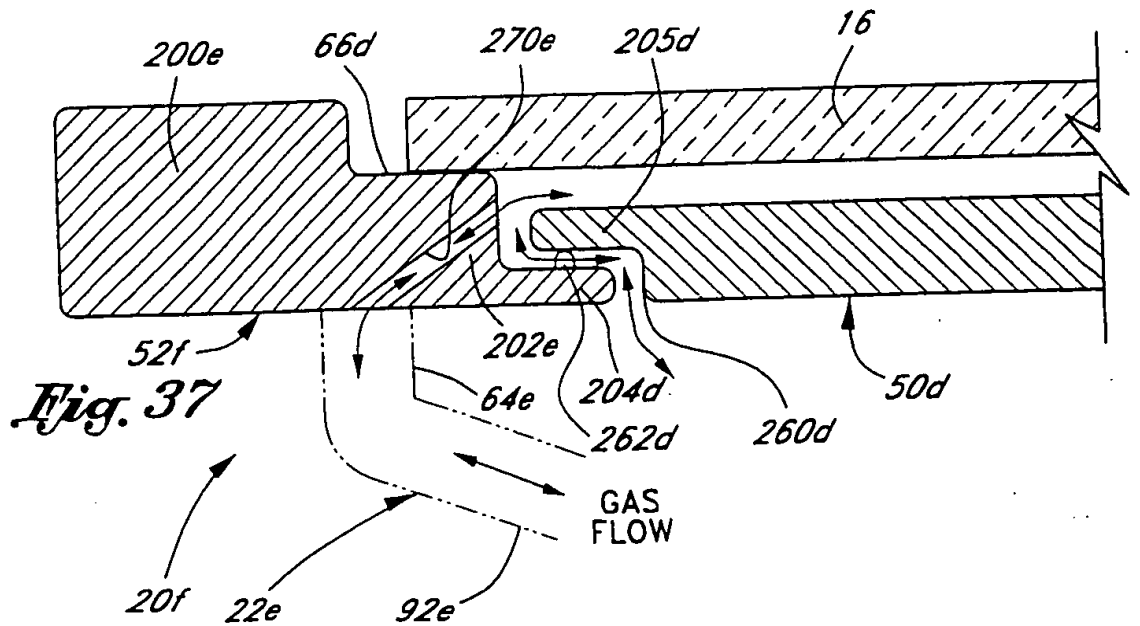
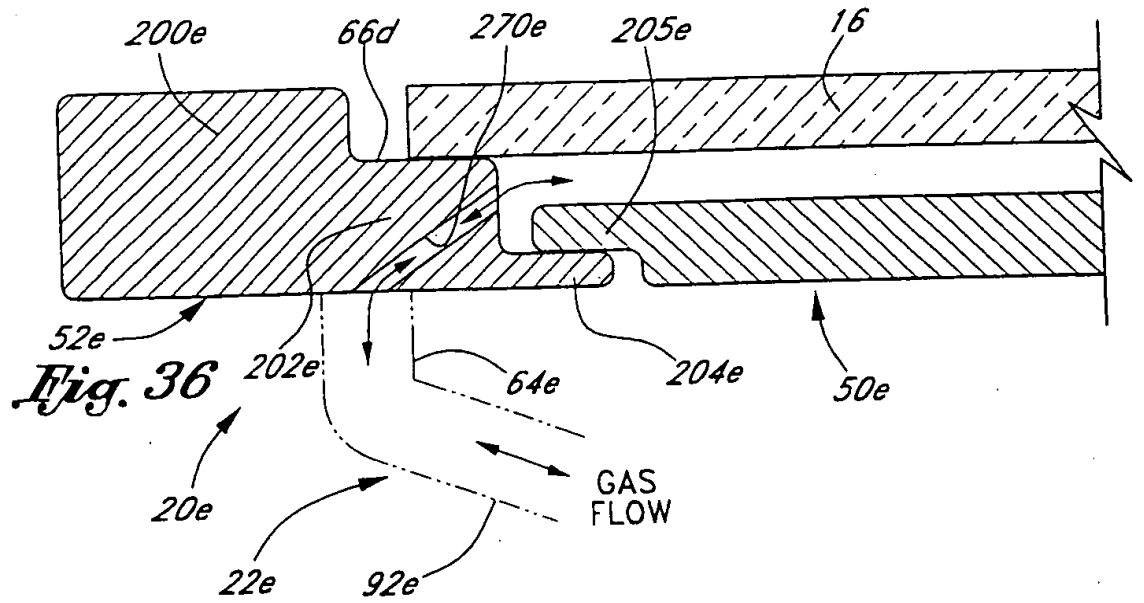
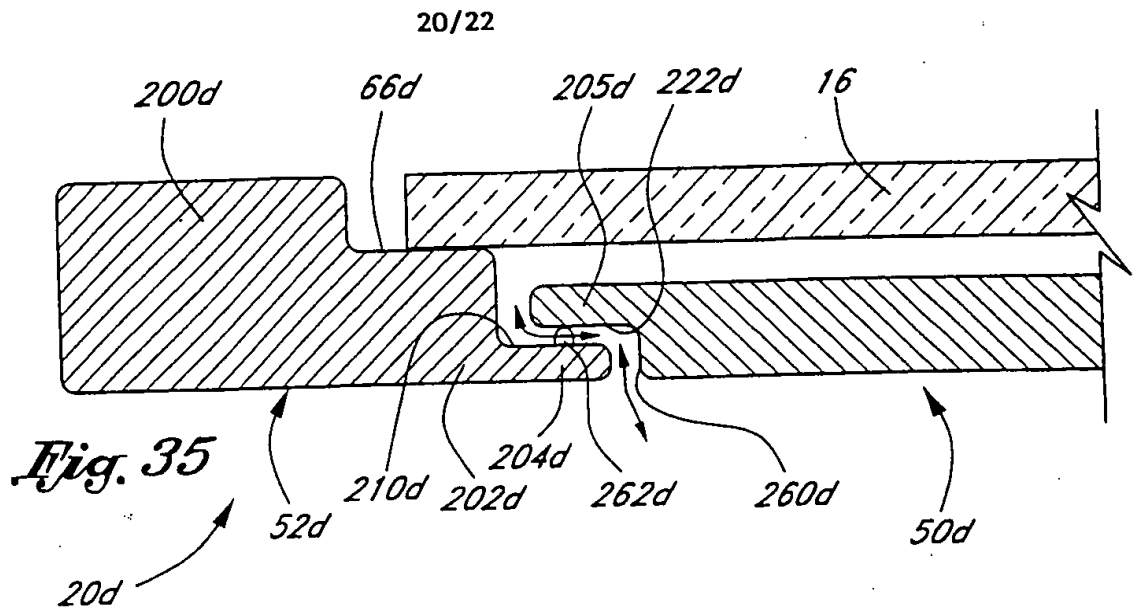


Fig. 34





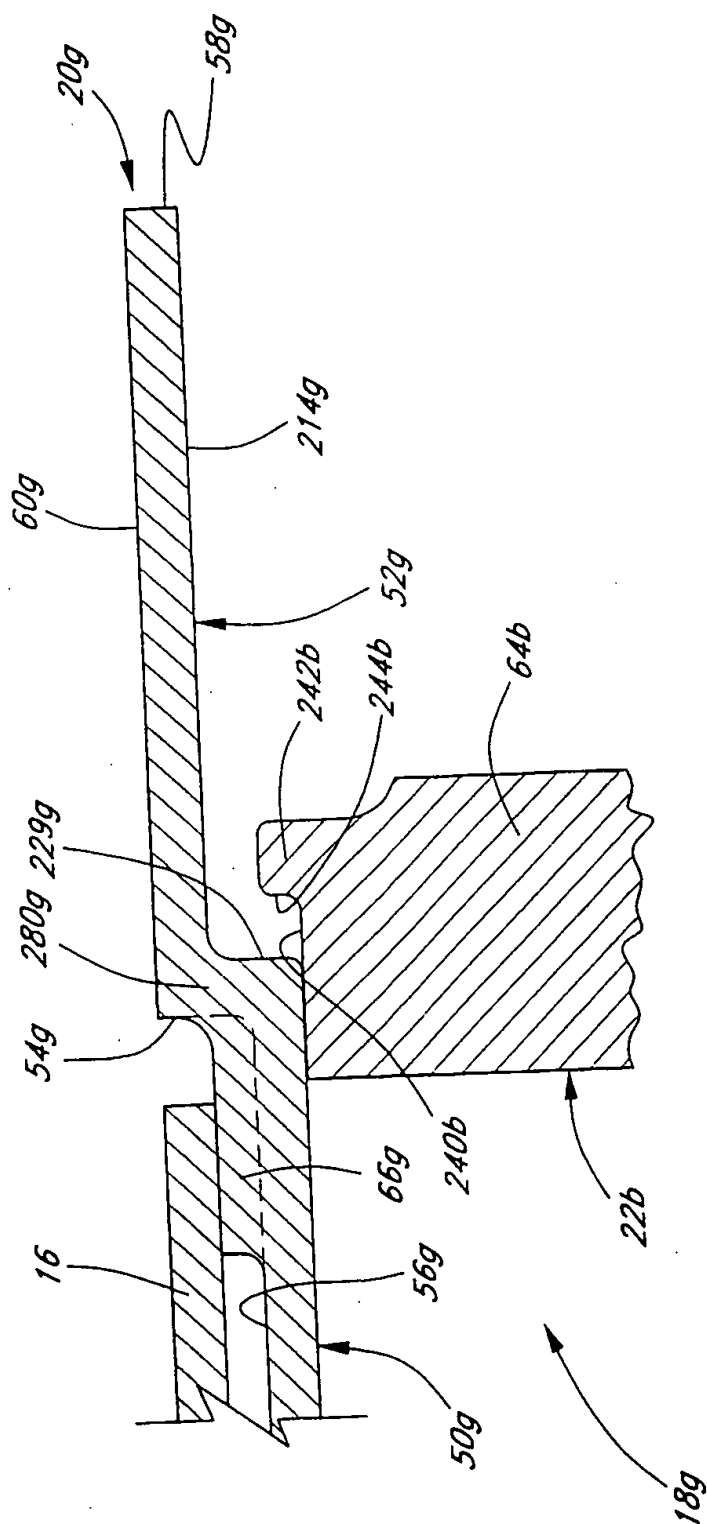


FIG. 38

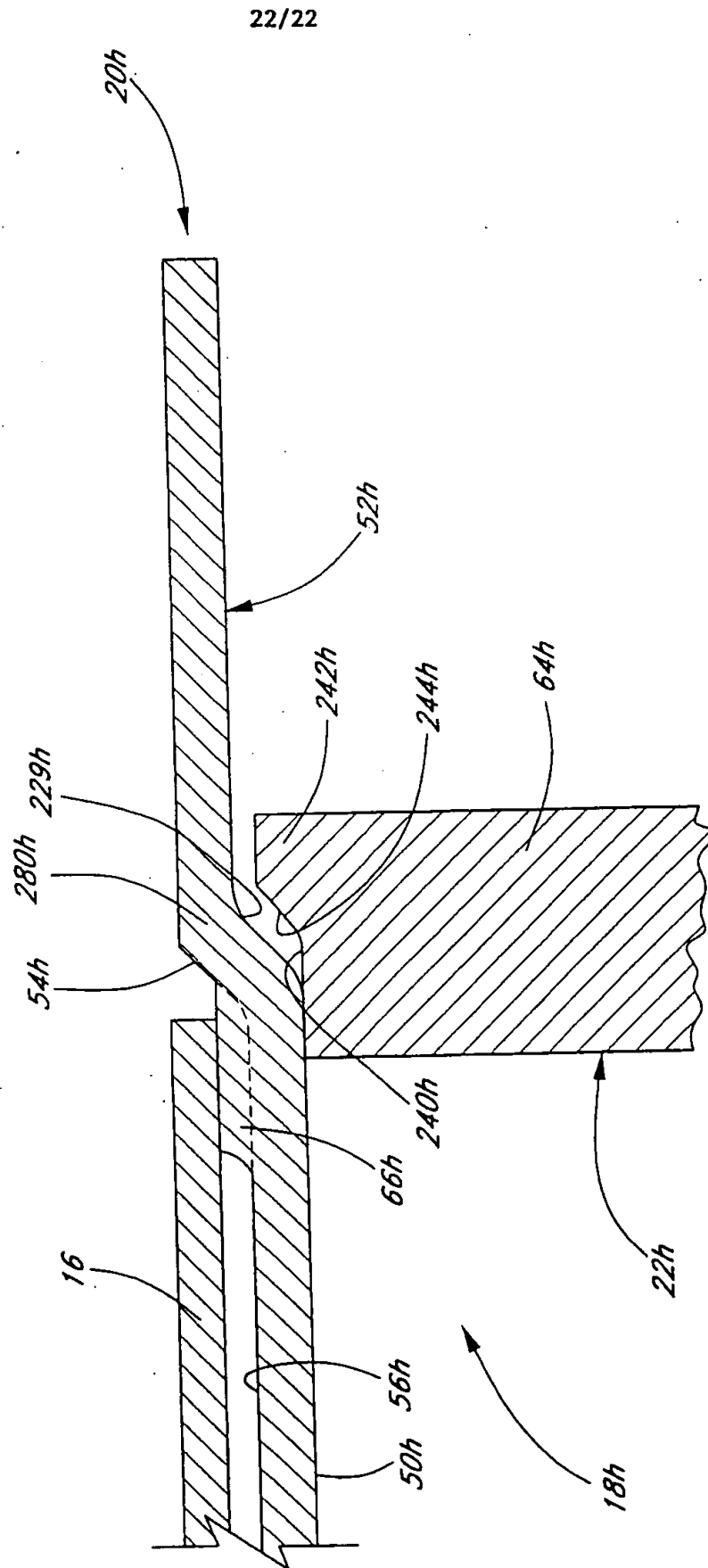
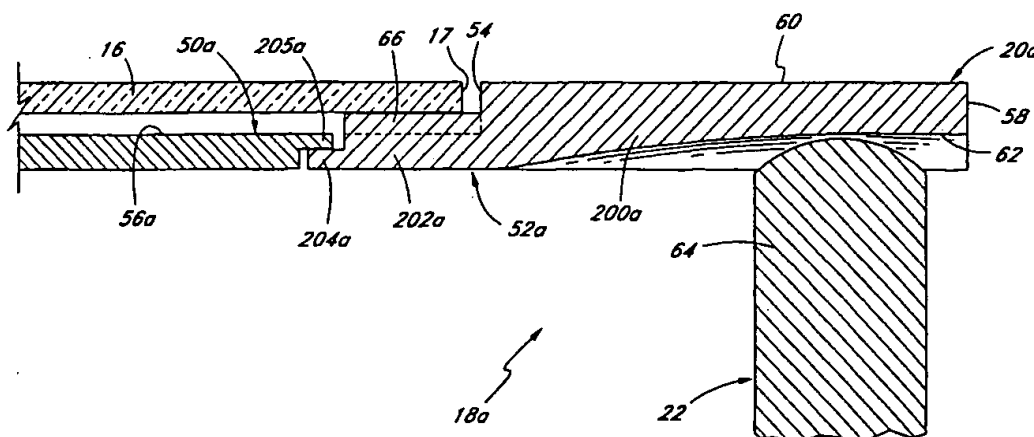


FIG. 39



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : H01L 21/00	A3	(11) International Publication Number: WO 99/23691 (43) International Publication Date: 14 May 1999 (14.05.99)
<p>(21) International Application Number: PCT/US98/23324</p> <p>(22) International Filing Date: 2 November 1998 (02.11.98)</p> <p>(30) Priority Data: 60/064,016 3 November 1997 (03.11.97) US</p> <p>(71) Applicant: ASM AMERICA, INC. [US/US]; 3440 East University Drive, Phoenix, AZ 85034-7200 (US).</p> <p>(72) Inventors: GOODMAN, Matthew, G.; 995 East Baseline #2032, Tempe, AZ 85283 (US). RAAIJMAKERS, Ivo; 2741 East Bighorn Avenue, Phoenix, AZ 85048 (US). JACOBS, Loren, R.; 2135 West Posada Avenue, Mesa, AZ 85202 (US). VAN BILSEN, Franciscus, B., M.; 5010 E. Cheyenne Drive #1114, Phoenix, AZ 85044 (US). MEYER, Michael, J.; 2344 East Aspen Drive, Tempe, AZ 85282 (US). BARRETT, Eric, Alan; 5053 East Downing, Mesa, AZ 85205 (US).</p> <p>(74) Agent: SIMPSON, Andrew, H.; Knobbe, Martens, Olson & Bear, LLP, 16th floor, 620 Newport Center Drive, Newport Beach, CA 92660 (US).</p>	<p>(81) Designated States: JP, KR, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p>Published <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i></p> <p>(88) Date of publication of the international search report: 5 August 1999 (05.08.99)</p>	

(54) Title: **IMPROVED LOW MASS WAFER SUPPORT SYSTEM**

(57) Abstract

Improvements in the design of a low mass wafer holder are disclosed. The improvements include the use of peripherally located, integral lips to space a wafer or other substrate above the base plate of the wafer holder. A uniform gap is thus provided between the wafer and the base plate, such as will temper rapid heat exchanges, allow gas to flow between the wafer and wafer holder during wafer pick-up, and keep the wafer holder thermally coupled with the wafer. At the same time, thermal disturbance from lip contact with the wafer is reduced. Gas flow during pick-up can be provided through radial channels in a wafer holder upper surface, or through backside gas passages. A thicker ring is provided at the wafer holder perimeter, and is provided in some embodiments as an independent piece to accommodate stresses accompanying thermal gradients. A self-centering mechanism is provided to keep the wafer holder centered relative to a spider which is subject to differential thermal expansion.

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INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 98/23324

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H01L21/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	EP 0 448 346 A (KABUSHIKI KAISHA TOSHIBA) 25 September 1991 see abstract; figures 3-34	1,3,4, 42,50 2,5-7, 13,16, 17,23, 46,49, 55,59,61
X A	EP 0 634 785 A (APPLIED MATERIALS) 18 January 1995 see abstract; figure 4	42 1,3,13, 55,59

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INTERNATIONAL SEARCH REPORT

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X	WO 97 08743 A (ADVANCED SEMICONDUCTOR MATERIALS AMERICA) 6 March 1997 see page 4, line 17-24; figure 12 see page 5, line 28-29 see page 6, line 21	1,2,9, 10,13-17
Y A	---	59-61 3-8,11, 12,18, 19,28, 42-47, 50,55, 56,62
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INTERNATIONAL SEARCH REPORT

International application No.
PCT/US 98/23324

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This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

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This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

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1-28, 42-50, 55-66

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- ☐ The additional search fees were accompanied by the applicant's protest.
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INTERNATIONAL SEARCH REPORT

International Application No. PCT/US 98/23324

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

1. Claims: 1-28,42-50,55-66

New arrangement on a low-mass wafer holder.

2. Claims: 29-41

New arrangement for supporting a wafer holder.

3. Claims: 51-54

Method for sensing the temperature at a point vertically spaced from the wafer holder.

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 98/23324

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